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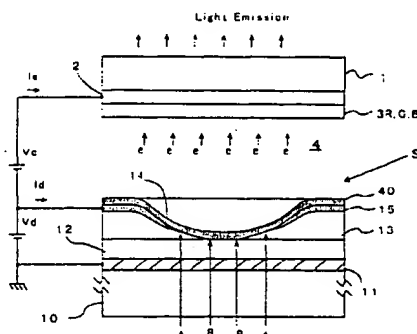
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(54) **Electron-emitting device and method of manufacturing the same and display apparatus using the same**

(57) An electron-emitting device includes an electron source layer (12) made of a metal, a metal alloy or a semiconductor, an insulating layer (13) formed on the electron source layer and a metal thin film electrode (15) formed on the insulating layer. Electrons are emitted upon application of an electric field between the electron source layer and the metal thin film electrode. The insu-

lating layer has at least one island region (14) which constitutes an electron-emitting section in which the film thickness of the insulating layer is gradually reduced. The electron-emitting device further includes a carbon region (40) made of carbon or a carbon compound on at least one of a top, bottom and inside of the island region.

Fig. 1



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## Description

### BACKGROUND OF THE INVENTION

[0001] The present invention relates to an electron-emitting device and a display apparatus using the same, especially to a flat panel display apparatus in which a plurality of the electron-emitting devices are arranged in an image display array, i.e. in a matrix arrangement.

[0002] As flat panel display apparatus, a field emission display (FED) comprising field electron-emitting devices is known. One of the known flat light-emitting displays uses a cold-cathode type electron-emitting source array in which heating of cathodes is not required. For instance, according to the principle of the light emission in an FED using a Spindt-type cold cathode, the light emission is achieved by drawing electrons out to a vacuum by a gate electrode disposed apart from a cathode, and having those electrons collided with a phosphor applied on a transparent anode in the same manner as a CRT (cathode ray tube) although there is a difference as the FED employs the cold cathode.

[0003] However, this field emission source has a problem of low manufacturing production since it requires a large number of complicated manufacturing processes for the minute Spindt-type cold cathodes.

[0004] There also are electron-emitting devices having metal-insulator-metal (MIM) structures as surface electron sources. One of such MIM-type electron-emitting devices has a structure comprising an Al layer, an  $\text{Al}_2\text{O}_3$  insulating layer having a thickness of about 10 nm, and an Au layer having a thickness of about 10 nm that are sequentially formed on a substrate as a cathode. When this structure is placed under a counter electrode within a vacuum, and a voltage is applied between the bottom Al layer and the top Au layer in conjunction with the application of an accelerating voltage to the counter electrode, then a part of the electrons from the top Au layer are emitted and they are accelerated and come in contact with the counter electrode. In this light-emitting device also, the light emission is achieved due to the electrons impinging on the phosphor applied on the counter electrode.

[0005] However, the amount of the electron emission is not quite sufficient even with the use of such MIM-type electron-emitting devices. To improve this emission, it has been believed that it is necessary to reduce the film thickness of the prior art  $\text{Al}_2\text{O}_3$  insulating layer by several nanometers, and to give further uniformity to the film quality of the ultra-thin  $\text{Al}_2\text{O}_3$  insulating layer and the interface between the  $\text{Al}_2\text{O}_3$  insulating layer and the top Au layer.

[0006] There have been attempts to improve the electron emission characteristics such as an invention described in, for example, JP-A-7-65710 employing an anodization method in which the forming current is controlled to further reduce the film thickness and improve the uniformity of the insulating layer. However, even with

such an MIM-type electron-emitting device manufactured according to this method, it has been only possible to achieve an emission current of, as much as about  $1 \times 10^{-5} \text{ A/cm}^2$  and an electron emission efficiency on the order of about 0.1%.

[0007] In an MIM-type electron-emitting device having an insulating layer as thick as several tens of nanometers to several micrometers, a uniform forming state cannot be obtained two-dimensionally, and there is a problem in that its electron emission characteristic is unstable. In general, an MIM device or an MIS-type electron-emitting device having an insulating layer as thick as several tens of nanometers to several micrometers is not yet capable, as manufactured, of providing electron emission. A process called "forming" is required, in which a voltage is applied between it and the ohmic electrode so as the metal thin film electrode would become a positive pole. The forming process differs from so-called electrical breakdown, and it has not yet been clearly explained although there have been various hypotheses such as those attempting to explain it as; the diffusion of electrode material into the insulating layer; the crystallization within the insulating layer; the growth of electroconductive paths called filaments; the stoichiometric deviation of the insulator composition and so forth. The controllability of this forming process is extremely low, and it is difficult to manufacture the devices with high stability and good reproducibility. Moreover, there is a fact that the growing locations of such forming sites are contingent across the electrode surface, so that originating points of electron emission (electron emission source) cannot be specified. In other words, the originating points of the electron emission cannot be formed homogeneously across the surface of the device, resulting in poor uniformity of the electron emission pattern.

[0008] Moreover, as another electron-emitting device, there is a surface-conduction electron-emitting device wherein cracks constituting electron-emitting sections are provided within an electroconductive thin film through electrification after laying the electroconductive thin film between counter electrodes provided on insulating substrates. These cracks are the sections of the electroconductive thin film that have locally been destroyed, transformed or deformed, so that there are problems in that, it has unevenness and poor geometric reproducibility, the shapes of the electron emitting portions are limited to linear shapes and so forth.

### SUMMARY OF THE INVENTION

[0009] The object of the invention, which was invented in consideration of the above background, is to provide an electron-emitting device capable of providing stable electron emission with a low voltage, and a display apparatus such as a flat panel display apparatus using such electron-emitting devices.

[0010] An electron-emitting device of the invention

comprises an electron source layer made of a metal, a metal compound or a semiconductor, an insulating layer formed on the electron source layer and a metal thin film electrode formed on the insulating layer, and emits electrons when an electric field is applied between the electron source layer and the metal thin film electrode, and the device is characterized in that the insulating layer has at least one island region constituting an electron-emitting section in which the film thickness of the insulating layer is gradually reduced, and a carbon region made of carbon or a carbon compound is provided either on the top or bottom, or inside of the island region.

[0011] In the electron-emitting device according to one aspect of the invention, said metal thin film electrode and said carbon region are deposited by one of a physical deposition method and a chemical deposition method.

[0012] In the electron-emitting device according to one aspect of the invention, said carbon region is a thin film deposited on one of said island region and said metal thin film electrode.

[0013] In the electron-emitting device according to one aspect of the invention, said carbon region is a thin film deposited on said island region while a voltage is being applied between said electron source layer and said metal thin film electrode.

[0014] In the electron-emitting device according to one aspect of the invention, said applied voltage is supplied intermittently according to a voltage application period in which the voltage rises and falls.

[0015] In the electron-emitting device according to one aspect of the invention, said carbon region is distributed within said metal thin film electrode.

[0016] In the electron-emitting device according to one aspect of the invention, said carbon region is a thin film deposited under said metal thin film electrode.

[0017] In the electron-emitting device according to one aspect of the invention, said carbon region is a thin film deposited under said insulating layer.

[0018] In the electron-emitting device according to one aspect of the invention, the thickness of said metal thin film is gradually reduced in conjunction with said insulating layer.

[0019] In the electron-emitting device according to one aspect of the invention, the thickness of said carbon region is gradually reduced in conjunction with said insulating layer.

[0020] In the electron-emitting device according to one aspect of the invention, said insulating layer is made of a dielectric material and has a thickness of at least 50 nm in areas other than said island region.

[0021] In the electron-emitting device according to one aspect of the invention, said metal thin film electrode terminates on said insulating layer within said island region.

[0022] In the electron-emitting device according to one aspect of the invention, said insulating layer terminates on said electron source layer within said island

region.

[0023] In the electron-emitting device according to one aspect of the invention, said island region is a recess on a flat surface of said metal thin film electrode and said insulating layer.

[0024] In the electron-emitting device according to one aspect of the invention, the device further comprises a fine particle within said island region.

[0025] In the electron-emitting device according to one aspect of the invention, the device further comprises, within said island region, a reverse-tapered block projecting in a direction normal to said substrate and at a top portion thereof, includes an overhang projecting in a direction parallel to said substrate.

[0026] A method for manufacturing an electron-emitting device according to the invention comprises:

an electron source layer formation process for forming an electron source layer made of one of a metal, a metal alloy and a semiconductor on a substrate; a mask formation process for forming masks on said electron source layer, each of said masks providing a shade around an area in which the masks contact with said electron source layer; an insulating layer formation process for depositing an insulating layer over said electron source layer and said masks so as to provide said insulating layer as a thin film of an insulator, said insulating layer having island regions in which film thickness of said insulating layer is gradually reduced in the proximity of the contact areas of said masks; and a metal thin film electrode formation process for forming a metal thin film electrode over said insulating layer, thereby constituting said island regions as electron-emitting sections;

said manufacturing method further comprises a process for providing a carbon region made of one of carbon and a carbon compound proximal to said island regions.

[0027] In the method according to one aspect of the invention, the method further comprises a mask elimination process for eliminating said masks immediately after said metal thin film electrode formation process, and said process for providing said carbon region is performed immediately after said mask elimination process, thereby forming said carbon region as a thin film deposited over said metal thin film electrode.

[0028] In the method according to one aspect of the invention, said process for providing said carbon region is performed by depositing said carbon region as a thin film while applying a voltage between said electron source layer and said metal thin film electrode.

[0029] In the method according to one aspect of the invention, said applied voltage is supplied intermittently according to a voltage application period in which the voltage rises and falls.

[0030] In the method according to one aspect of the invention, the method further comprises a mask elimi-

nation process for eliminating said masks immediately after said insulating layer formation process, and said process for providing said carbon region is performed during said metal thin film electrode formation process, thereby having said carbon region distributed within said metal thin film electrode.

**[0031]** In the method according to one aspect of the invention, said process for providing said carbon region is performed immediately after said metal thin film electrode formation process, thereby forming said carbon region as a thin film deposited over said metal thin film electrode.

**[0032]** In the method according to one aspect of the invention, said process for providing said carbon region is performed immediately before said metal thin film electrode formation process, thereby forming said carbon region as a thin film deposited under said metal thin film electrode.

**[0033]** In the method according to one aspect of the invention, said process for providing said carbon region is performed immediately before said insulating layer formation process, thereby forming said carbon region as a thin film deposited under said insulating layer.

**[0034]** In the method according to one aspect of the invention, the method further comprises a mask elimination process for eliminating said masks immediately after said metal thin film electrode formation process, and said process for providing said carbon region is performed immediately after said mask elimination process, thereby forming said carbon region as a thin film deposited over said metal thin film electrode.

**[0035]** In the method according to one aspect of the invention, a voltage is applied between said electron source layer and said metal thin film electrode immediately after at least one of said process for providing said carbon region, said metal thin film electrode formation process, and said mask elimination process.

**[0036]** In the method according to one aspect of the invention, said masks are fine particles, and said mask formation process comprises a step of spraying said fine particles onto said electron source layer.

**[0037]** In the method according to one aspect of the invention, each of said masks is an electrically insulating reverse-tapered block which projects outwardly in a direction normal to said substrate and has an overhang in a top portion thereof, projecting in a direction parallel to said substrate, and said mask formation process includes steps of:

forming a reverse-tapered block material layer on said substrate;

forming thereon a resist mask which allows at least a part of said electron source layer to be exposed through a photolithographic method; and

etching out said reverse-tapered block having said overhang by one of a dry etching method and a wet etching method.

**[0038]** In the method according to one aspect of the invention, said insulating layer, said metal thin film electrode and said carbon region are deposited by a physical deposition method or a chemical deposition method.

**[0039]** A display apparatus according to the present invention comprises;

a first substrate and a second substrate facing each other with a vacuum space therebetween;

a plurality of electron-emitting devices provided on said first substrate;

a collector electrode provided on an interior surface of said second substrate; and

a phosphor layer formed on said collector electrode; wherein each of said electron-emitting devices comprises an electron source layer made of one of a metal, a metal alloy and a semiconductor formed on an ohmic electrode, an insulating layer formed on said electron source layer and a metal thin film electrode formed on said insulating layer, said insulating layer having at least one island region constituting an electron-emitting section in which the film thickness of said insulating layer is gradually reduced, and a carbon region made of one of carbon and a carbon compound is provided on at least one of a top, bottom and inside of said island region.

**[0040]** In the display apparatus according to one aspect of the invention, said insulating layer, said metal thin film electrode and said carbon region are deposited by one of a physical deposition method and a chemical deposition method.

**[0041]** In the display apparatus according to one aspect of the invention, said carbon region is a thin film deposited on one of said island region and said metal thin film electrode.

**[0042]** In the display apparatus according to one aspect of the invention, said carbon region is a thin film deposited on said island region while a voltage is being applied between said electron source layer and said metal thin film electrode.

**[0043]** In the display apparatus according to one aspect of the invention, said applied voltage is supplied intermittently according to a voltage application period in which the voltage rises and falls.

**[0044]** In the display apparatus according to one aspect of the invention, said carbon region is distributed within said metal thin film electrode.

**[0045]** In the display apparatus according to one aspect of the invention, said carbon region is a thin film deposited under said metal thin film electrode.

**[0046]** In the display apparatus according to one aspect of the invention, said carbon region is a thin film deposited under said insulating layer.

**[0047]** In the display apparatus according to one aspect of the invention, the thickness of said metal thin film electrode is gradually reduced in conjunction with said insulating layer.

[0048] In the display apparatus according to one aspect of the invention, the thickness of said carbon region is gradually reduced in conduction with said insulating layer.

[0049] In the display apparatus according to one aspect of the invention, said insulating layer is made of a dielectric material and has a film thickness of at least 50 nm in areas other than said island region.

[0050] In the display apparatus according to one aspect of the invention, said metal thin film electrode terminates on said insulating layer within said island region.

[0051] In the display apparatus according to one aspect of the invention, said insulating layer terminates on said electron source layer within said island region.

[0052] In the display apparatus according to one aspect of the invention, said island region is a recess on a flat surface of said metal thin film electrode and said insulating layer.

[0053] In the display apparatus according to one aspect of the invention, the display apparatus further comprises a fine particle within said island region.

[0054] In the display apparatus according to one aspect of the invention, the display apparatus further comprises, within said island region, a reverse-tapered block which projects outwardly in a direction normal to said substrate and has an overhang in a top portion thereof, projecting in a direction parallel to said substrate.

[0055] In the display apparatus according to one aspect of the invention, bus lines are formed over a plurality of said metal thin film electrodes, and said ohmic electrodes and said bus lines are electrodes, each having a shape of a strip, and arranged orthogonal to each other.

[0056] According to the present invention having the above any configuration, in the direction to which the interface between the insulating layer and the metal thin film layer extends, a carbon region made of carbon or a carbon compound is provided either on the top or bottom, or inside of each of a plurality of islands in which the film thicknesses of the insulating layer and the metal thin film electrode are gradually reduced respectively, so that an electron-emitting device in which the amount of electrons emitted from those island regions are increased may be obtained.

[0057] Moreover, according to the electron-emitting device of the invention, since the insulating layer has a large film thickness in the portion other than the island regions, through-holes are unlikely to occur, which in turn, would improve the manufacturing yield. Furthermore, the electron-emitting device of the invention may be implemented as a high-speed device such as a light-emitting source of a pixel valve, an electron-emitting source of an electron microscope, and a vacuum micro-electronic element etc., and it is also operable as a surface-type or dot-type electron-emitting diode, a light-emitting diode or a laser diode emitting electromagnetic waves of millimeter-scale or submillimeter-scale, as well

as a high-speed switching element.

#### BRIEF DESCRIPTION OF THE DRAWINGS

5 [0058] Fig. 1 is a schematic cross-sectional view of an electron-emitting device of the invention.

[0059] Figs. 2 through 8 are expanded views of a portion of a device substrate during fabrication according to a manufacturing method of the electron-emitting device of the invention.

10 [0060] Fig. 9 is an expanded view of a portion of a device substrate during fabrication according to a manufacturing method of another electron-emitting device of the invention.

15 [0061] Figs. 10 through 18 are expanded views of a portion of alternative electron-emitting devices of the invention.

[0062] Figs. 19 and 20 are expanded perspective views of a portion of a device substrate during fabrication according to a manufacturing method of another electron-emitting device of the invention.

20 [0063] Fig. 21 is an expanded perspective view of another electron-emitting device of the invention.

[0064] Figs. 22 and 23 are expanded perspective views of the device substrate during fabrication according to a manufacturing method of another electron-emitting device of the invention.

[0065] Fig. 24 is an expanded perspective view of another electron-emitting device of the invention.

30 [0066] Fig. 25 is a graphical representation showing the variation in the diode current  $I_d$ , emission current  $I_e$  and efficiency of the electron-emitting device according to one embodiment, plotted against the number of the repetition of the carbon-type film formation processes.

35 [0067] Fig. 26 is a graphical representation showing the variation in the diode current  $I_d$ , emission current  $I_e$  and efficiency of a comparative electron-emitting device plotted against the number of the repetition of the electrification processes.

40 [0068] Fig. 27 is a graphical representation of the variation in the diode current  $I_d$ , emission current  $I_e$  and efficiency of the electron-emitting device of the embodiment plotted against the thickness of the carbon-type film.

45 [0069] Fig. 28 is an expanded perspective view of a portion of a device substrate during the fabrication of another electron-emitting device of the invention.

[0070] Fig. 29 is an expanded perspective view of the portion of another electron-emitting device of the invention.

50 [0071] Figs. 30 and 31 are expanded perspective views of a portion of a device substrate during fabrication according to still another manufacturing method of an electron-emitting device of the invention.

55 [0072] Fig. 32 is an expanded perspective view of a portion of another electron-emitting device of the invention.

[0073] Fig. 33 is a schematic perspective view show-

ing a portion of an embodiment of an electron-emitting-device flat panel display apparatus according to the invention.

**[0074]** Fig. 34 is an expanded schematic cross-sectional view showing a portion of the embodiment of the electron-emitting-device flat panel display apparatus taken along the line AA of Fig. 33.

**[0075]** Fig. 35 is a schematic cross-sectional view of an embodiment of an electron-emitting light-emitting device according to the invention.

**[0076]** Fig. 36 is a schematic cross-sectional view of another embodiment of an electron-emitting light-emitting device according to the invention.

#### DETAILED DESCRIPTION OF THE INVENTION

**[0077]** Now, embodiments of the invention will be explained with reference to figures.

{Electron-Emitting Device}

**[0078]** As shown in Fig. 1, an electron-emitting device S of the invention in one embodiment comprises an ohmic electrode 11 made of aluminum (Al), tungsten (W), titanium nitride (TiN), copper (Cu) or chrome (Cr) etc. formed over i.e. a glass substrate 10, an electron source layer 12 made of a metal, a metal compound or silicon (Si) etc. formed thereon, an insulating layer 13 made of  $\text{SiO}_x$  ( $x=0.1-2.0$ ) etc. formed thereon, a metal thin film electrode 15 made of, i.e. white gold (Pt) or gold (Au) etc. formed thereon, and a carbon region made of carbon or a carbon compound formed at least over a recessed portion 14. As for the material of the carbon region 40, forms of carbon such as amorphous carbon, graphite, carbyne, fullerene ( $\text{C}_{2n}$ ), diamond-like carbon, carbon nano-tube, and diamond etc., or carbon compounds such as  $\text{ZrC}$ ,  $\text{SiC}$ ,  $\text{WC}$ , and  $\text{MoC}$  etc. are effective.

**[0079]** The insulating layer 13 is made of a dielectric material, and its thickness in the flat portion is 50 nm or greater which is extremely large. This layer is formed through a sputtering method with the use of Ar, Kr or Xe gas, or the combination thereof, or a mixed gas containing these rare gases as the main component with  $\text{O}_2$  or  $\text{N}_2$  mixed thereto, under a sputtering condition; gas pressure = 0.1 - 100 mTorr, preferably 0.1 - 20 mTorr, and growth rate = 0.1 - 1000 nm/min, preferably 0.5 - 100 nm/min.

**[0080]** In the insulating layer 13 and the metal thin film electrode 15, a recess 14, that is, an island region 14 in which the film thickness of both the films are gradually reduced toward the center, is formed. As shown in Fig. 1, the island region 14 is formed as a circular concave region in the flat surface of the metal thin film electrode 15, and on this island region 14, the carbon region is deposited. In the island region 14, the metal thin film electrode 15 is terminated on the insulating layer 13 at an edge position A. The insulating layer 13 is also ter-

minated within the island region 14, on the electron source layer 12 at an edge position B. The carbon region 40 covers the metal thin film electrode 15, insulating layer 13 and electron source layer 12.

**[0081]** As for the material of the electron source layer 12 of the electron-emitting device, Si is especially effective, however, it is also possible to use amorphous silicon (a-Si), hydrogenated amorphous silicon (a-Si:H) in which the dangling bonds of a-Si are terminated with hydrogen (H), or a compound semiconductor such as hydrogenated amorphous silicon carbide (a-SiC:H) in which a part of Si is replaced with carbon (C) or hydrogenated amorphous silicon nitride (a-SiN:H) in which a part of Si is replaced with nitrogen (N), or silicon doped with boron, gallium, phosphorus, indium, arsenic, or antimony. Instead of using Si, a single semiconductor or a compound semiconductor of group IV, III-V, or II-VI such as germanium (Ge), Ge-Si, silicon carbide (SiC), gallium arsenide (GaAs), indium phosphide (InP), cadmium selenide or  $\text{CuInTe}_2$  etc. may also be used for the electron source layer.

**[0082]** Metals such as Al, Au, Ag and Cu etc. are also effective for the material of the electron source layer, but it is also possible to use Sc, Ti, Cr, Mn, Fe, Co, Ni, Zn, Ga, Y, Zr, Nb, Mo, Tc, Ru, Rh, Pd, Cd, Ln, Sn, Ta, W, Re, Os, Ir, Pt, Tl, Pb, La, Ce, Pr, Nd, Nd, Pm, Sm, Eu, Gd, Tb, Dy, Ho, Er, Tm, Yb, Lu or the like.

**[0083]** As for the dielectric material of the insulating layer 13, although silicon oxide ( $\text{SiO}_x$ ) ( $x$  represents an atomic ratio) is especially effective, it may be a metal oxide or a metal nitride such as  $\text{LiO}_x$ ,  $\text{LiN}_x$ ,  $\text{NaO}_x$ ,  $\text{KO}_x$ ,  $\text{RbO}_x$ ,  $\text{CsO}_x$ ,  $\text{BeO}_x$ ,  $\text{MgO}_x$ ,  $\text{MgN}_x$ ,  $\text{CaO}_x$ ,  $\text{CaN}_x$ ,  $\text{SrO}_x$ ,  $\text{BaO}_x$ ,  $\text{ScO}_x$ ,  $\text{YO}_x$ ,  $\text{YN}_x$ ,  $\text{LaO}_x$ ,  $\text{LaN}_x$ ,  $\text{CeO}_x$ ,  $\text{PrO}_x$ ,  $\text{NdO}_x$ ,  $\text{SmO}_x$ ,  $\text{EuO}_x$ ,  $\text{GdO}_x$ ,  $\text{TbO}_x$ ,  $\text{DyO}_x$ ,  $\text{HoO}_x$ ,  $\text{ErO}_x$ ,  $\text{TmO}_x$ ,  $\text{YbO}_x$ ,  $\text{LuO}_x$ ,  $\text{TiO}_x$ ,  $\text{ZrO}_x$ ,  $\text{ZrN}_x$ ,  $\text{HfO}_x$ ,  $\text{HfN}_x$ ,  $\text{ThO}_x$ ,  $\text{VO}_x$ ,  $\text{VN}_x$ ,  $\text{NbO}_x$ ,  $\text{NbN}_x$ ,  $\text{TaO}_x$ ,  $\text{TaN}_x$ ,  $\text{CrO}_x$ ,  $\text{CrN}_x$ ,  $\text{MoO}_x$ ,  $\text{MoN}_x$ ,  $\text{WO}_x$ ,  $\text{WN}_x$ ,  $\text{MnO}_x$ ,  $\text{ReO}_x$ ,  $\text{FeO}_x$ ,  $\text{FeN}_x$ ,  $\text{RuO}_x$ ,  $\text{OsO}_x$ ,  $\text{CoO}_x$ ,  $\text{RhO}_x$ ,  $\text{IrO}_x$ ,  $\text{NiO}_x$ ,  $\text{PdO}_x$ ,  $\text{PtO}_x$ ,  $\text{CuO}_x$ ,  $\text{CuN}_x$ ,  $\text{AgO}_x$ ,  $\text{AuO}_x$ ,  $\text{ZnO}_x$ ,  $\text{CdO}_x$ ,  $\text{HgO}_x$ ,  $\text{BO}_x$ ,  $\text{BN}_x$ ,  $\text{AlO}_x$ ,  $\text{AlN}_x$ ,  $\text{GaO}_x$ ,  $\text{GaN}_x$ ,  $\text{InO}_x$ ,  $\text{SiN}_x$ ,  $\text{GeO}_x$ ,  $\text{SnO}_x$ ,  $\text{PbO}_x$ ,  $\text{PO}_x$ ,  $\text{PN}_x$ ,  $\text{AsO}_x$ ,  $\text{SbO}_x$ ,  $\text{SeO}_x$ ,  $\text{TeO}_x$  or the like.

**[0084]** It is also effective, for the dielectric material of the insulating film 13, to use a complex metal oxide such as  $\text{LiAlO}_2$ ,  $\text{Li}_2\text{SiO}_3$ ,  $\text{Li}_2\text{TiO}_3$ ,  $\text{Na}_2\text{Al}_2\text{O}_3$ ,  $\text{NaFeO}_2$ ,  $\text{Na}_4\text{SiO}_4$ ,  $\text{K}_2\text{SiO}_3$ ,  $\text{K}_2\text{TiO}_3$ ,  $\text{K}_2\text{WO}_4$ ,  $\text{Rb}_2\text{CrO}_4$ ,  $\text{CS}_2\text{CrO}_4$ ,  $\text{MgAl}_2\text{O}_4$ ,  $\text{MgFe}_2\text{O}_4$ ,  $\text{MgTiO}_3$ ,  $\text{CaTiO}_3$ ,  $\text{CaWO}_4$ ,  $\text{CaZrO}_3$ ,  $\text{SrFe}_{12}\text{O}_{19}$ ,  $\text{SrTiO}_3$ ,  $\text{SrZrO}_3$ ,  $\text{BaAl}_2\text{O}_4$ ,  $\text{BaFe}_{12}\text{O}_{19}$ ,  $\text{BaTiO}_3$ ,  $\text{Y}_3\text{Al}_5\text{O}_{12}$ ,  $\text{Y}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{LaFeO}_3$ ,  $\text{La}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{La}_2\text{Ti}_2\text{O}_7$ ,  $\text{CeSnO}_4$ ,  $\text{CeTiO}_4$ ,  $\text{Sm}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{EuFeO}_3$ ,  $\text{Eu}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{GdFeO}_3$ ,  $\text{Gd}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{DyFeO}_3$ ,  $\text{Dy}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{HoFeO}_3$ ,  $\text{Ho}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{ErFeO}_3$ ,  $\text{Er}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{Tm}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{LuFeO}_3$ ,  $\text{Lu}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{NiTiO}_3$ ,  $\text{Al}_2\text{TiO}_3$ ,  $\text{FeTiO}_3$ ,  $\text{BaZrO}_3$ ,  $\text{LiZrO}_3$ ,  $\text{MgZrO}_3$ ,  $\text{HfTiO}_4$ ,  $\text{NH}_4\text{VO}_3$ ,  $\text{AgVO}_3$ ,  $\text{LiVO}_3$ ,  $\text{BaNb}_2\text{O}_6$ ,  $\text{NaNbO}_3$ ,  $\text{SrNb}_2\text{O}_6$ ,  $\text{KTaO}_3$ ,  $\text{NaTaO}_3$ ,  $\text{SrTa}_2\text{O}_6$ ,  $\text{CuCr}_2\text{O}_4$ ,  $\text{Ag}_2\text{CrO}_4$ ,  $\text{BaCrO}_4$ ,  $\text{K}_2\text{MoO}_4$ ,  $\text{Na}_2\text{MoO}_4$ ,  $\text{NiMoO}_4$ ,  $\text{BaWO}_4$ ,  $\text{NaWO}_4$ ,  $\text{SrWO}_4$ ,  $\text{MnCr}_2\text{O}_4$ ,  $\text{MnFe}_2\text{O}_4$ ,  $\text{MnTiO}_3$ ,  $\text{MnWO}_4$ ,  $\text{CoFe}_2\text{O}_4$ ,  $\text{ZnFe}_2\text{O}_4$ ,  $\text{FeWO}_4$ ,  $\text{CoMoO}_4$ ,  $\text{CoTiO}_3$ ,  $\text{CoWO}_4$ ,  $\text{NiFe}_2\text{O}_4$ ,

NiWO<sub>4</sub>, CuFe<sub>2</sub>O<sub>4</sub>, CuMoO<sub>4</sub>, CuTiO<sub>3</sub>, CuWO<sub>4</sub>, Ag<sub>2</sub>MoO<sub>4</sub>, Ag<sub>2</sub>WO<sub>4</sub>, ZnAl<sub>2</sub>O<sub>4</sub>, ZnMoO<sub>4</sub>, ZnWO<sub>4</sub>, CdSnO<sub>3</sub>, CdTiO<sub>3</sub>, CdMoO<sub>4</sub>, CdWO<sub>4</sub>, NaAlO<sub>2</sub>, MgAl<sub>2</sub>O<sub>4</sub>, SrAl<sub>2</sub>O<sub>4</sub>, Gd<sub>3</sub>Ga<sub>5</sub>O<sub>12</sub>, InFeO<sub>3</sub>, MgIn<sub>2</sub>O<sub>4</sub>, AlTiO<sub>5</sub>, FeTiO<sub>3</sub>, MgTiO<sub>3</sub>, Na<sub>2</sub>SiO<sub>3</sub>, CaSiO<sub>3</sub>, ZrSiO<sub>4</sub>, K<sub>2</sub>GeO<sub>3</sub>, Li<sub>2</sub>GeO<sub>3</sub>, Na<sub>2</sub>GeO<sub>3</sub>, Bi<sub>2</sub>Sn<sub>3</sub>O<sub>9</sub>, MgSnO<sub>3</sub>, SrSnO<sub>3</sub>, PbSiO<sub>3</sub>, PbMoO<sub>4</sub>, PbTiO<sub>3</sub>, SnO<sub>2</sub>-Sb<sub>2</sub>O<sub>3</sub>, CuSeO<sub>4</sub>, Na<sub>2</sub>SeO<sub>3</sub>, ZnSeO<sub>3</sub>, K<sub>2</sub>TeO<sub>3</sub>, K<sub>2</sub>TeO<sub>4</sub>, Na<sub>2</sub>TeO<sub>3</sub>, Na<sub>2</sub>TeO<sub>4</sub> or the like, or a sulfide such as FeS, Al<sub>2</sub>S<sub>3</sub>, MgS or ZnS etc., a fluoride such as LiF, MgF<sub>2</sub> or SmF<sub>3</sub> etc., a chloride such as HgCl<sub>2</sub>, FeCl<sub>2</sub> or CrCl<sub>3</sub> etc., a bromide such as AgBr, CuBr or MnBr<sub>2</sub> etc., an iodide such as PbI<sub>2</sub>, CuI or FeI<sub>2</sub> etc., or a metal oxide nitride such as SiAlON or the like.

[0085] Furthermore, carbon such as diamond or fullerene (C<sub>2n</sub>), or a metal carbide such as Al<sub>4</sub>C<sub>3</sub>, B<sub>4</sub>C, CaC<sub>2</sub>, Cr<sub>3</sub>C<sub>2</sub>, Mo<sub>2</sub>C, MoC, NbC, SiC, TaC, TiC, VC, W<sub>2</sub>C, WC or ZrC etc. is also effective. Fullerene (C<sub>2n</sub>) is a spherical-shell like molecule made up of carbon atoms only, ranging from C<sub>32</sub> through C<sub>960</sub>, among which the most known is C<sub>60</sub>. The "x" suffix on the above terms such as "O<sub>x</sub>" or "N<sub>x</sub>" represents an atomic ratio.

[0086] The thickness of the insulating layer in the flat portion other than the island regions is 50 nm or greater, preferably from 100 nm to 1000 nm.

[0087] As for the material of the metal thin film electrode 15 on the electron-emitting side, metals such as Pt, Au, W, Ru or Ir are effective, but Be, C, Al, Si, Sc, Ti, V, Cr, Mn, Fe, Co, Ni, Cu, Zn, Ga, Y, Zr, Nb, Mo, Tc, Rh, Pd, Ag, Cd, In, Sn, Ta, Re, Os, Tl, Pb, La, Ce, Pr, Nd, Pm, Sm, Eu, Gd, Tb, Dy, Ho, Er, Tm, Yb or Lu etc. may also be used.

{Manufacturing Method of the Electron-Emitting Device}

[0088] As for the film formation method in manufacturing such electron-emitting devices, a physical or chemical method is used. The physical methods are known as physical vapor deposition (PVD) and include a vacuum deposition method, molecular beam epitaxy method, sputtering method, ionized vapor deposition method and laser abrasion method. The chemical methods are known as chemical vapor deposition (CVD) and include thermal CVD, plasma CVD and MOCVD (metal-organic chemical vapor deposition) etc. Among these methods, the sputtering method is especially effective.

[0089] The island region 14 of Fig. 1 which is the concave portion covered by the carbon region 40, is formed in the following manner. First, as shown in Fig. 2, the electron source layer 12 is formed through sputtering on the substrate 10 having the ohmic electrode 11 formed thereon. Next, a plurality of spherical fine particles 20 are homogeneously sprayed on the electron source layer as shown in Fig. 3. Although non-spherical shapes of the fine particles, which serve as masks, may also allow the electron emission to be obtained, spherical particles such as spacers for liquid crystal displays or ball mills having an isotropic geometry are preferred when con-

sidering their uniformity in grain boundaries and homogeneous distribution over the film, and lack of aggregation. In addition, a small grain size is preferred. The material of the fine particles may be an insulator, a semiconductor or a metal. When metallic fine particles are used, there is a possibility for the particles to cause short-circuits in the device, so that it is preferred to eliminate the particles after the formation of the metal thin film electrode 15.

[0090] Thereafter, as shown in Fig. 4, an insulator 13, 13a is formed on the electron source layer 12 and on the fine particles 20 to form an insulating layer 13 made of a thin film of the insulator. At this point, the insulator is deposited also on the area around the contact between the electron source layer 12 and the particle 20, thereby forming the portion of the insulating layer in which the film thickness is gradually reduced from the given thickness of the insulating layer 13. The portion of the insulating layer in which the film thickness is gradually reduced terminates on the electron source layer 12 at the edge position B within the island region 14.

[0091] A metal 15, 15a is then deposited over the insulating layer 13 and the particle 20 as shown in Fig. 5 to form the metal thin film electrode 15. At this point, the metal is deposited also around the contacting portion of the electron source layer 12 and the particle 20, thereby forming the portion of the metal thin film electrode in which its film thickness is gradually reduced from the given thickness of the metal thin film electrode 15. The portion of the metal thin film electrode 15 in which the film thickness is gradually reduced terminates on the insulating layer 13 at the edge position A. That is, there is a boundary between the particle 20 and the electron source layer 12 or the metal thin film electrode 15, and the film thickness of the insulating layer 13 and metal thin film electrode 15 are continuously reduced from that boundary toward the contact point between the particle and the electron source layer 12. In this way, the island region 14, which is a recess, is formed within the insulating layer 13 and the metal thin film electrode 15 around the contact plane under the particle 20.

[0092] After this metal thin film electrode formation process, the plurality of particles are eliminated through ultrasonic cleaning so that a plurality of island regions 14 of circular recesses are formed as shown in Fig. 6.

[0093] As shown in Figs. 7 and 8, a carbon region 40 is formed as a thin film over the island regions 14 and the metal thin film electrode 15 after the mask elimination process (particle elimination process).

[0094] For the formation process of the carbon region, the substrate on which the recessed island regions are formed is placed in a vacuum chamber as shown in Fig. 7, and a hydrogen carbide gas such as methane gas is introduced into the vacuum chamber 39, and voltage is applied between the electron source layer 12 and the metal thin film electrode 15 via the ohmic electrode 11 within the hydrogen carbide atmosphere which has been vacuumed to approximately 0.1 to 1 x 10<sup>-5</sup> Torr. In

this process, hydrogen carbide within the chamber is adsorbed to or deposited on, or reacted with the entire surface of the metal thin film electrode 15 and on the insulating layer 13 and the electron source layer 12 within the recessed island regions 14, thereby forming a thin film constituting the carbon region 40. It is preferred to set a voltage application period, and repeat the voltage application process for more than one cycle of the voltage application period.

**[0095]** The carbon region 40 may be formed as a thin film also by uniformly depositing it over the island regions and the metal thin film electrode 15 through sputtering with the use of a carbon target 41 as shown in Fig. 8.

**[0096]** Although the fine particles 20 were explained as being placed in contact with the electron source layer 12 in the case of the above embodiments, alternatively, a preliminary insulating layer 13b may be formed immediately before the particle dispersion process (Fig. 3) through sputtering to isolate the fine particles 20 and the electron source layer 12 by that preliminary insulating layer 13b as shown in Fig. 9. In a case where the preliminary insulating layer 13b is provided, its film thickness shall be within a range from several tens to several thousands angstroms. In this way, the risk of producing short circuits between the electron source layer 12 and the metal thin film electrode 15 may be avoided.

**[0097]** Furthermore, in the case of the above embodiments, the carbon region 40 was constituted by a thin film which is deposited so as to entirely cover the metal thin film electrode 15, insulating layer 13 and electron source layer 12, however, the carbon regions 40 may alternatively be formed so as to be terminated on the insulating layer 13 or on the metal thin film electrode 15 within the island regions 14 as shown in Fig. 10. In this case, the formation process of the carbon region is performed immediately after the formation of the metal thin film electrode 15 (Fig. 5), and the carbon region 40 is deposited as a thin film on the metal thin film electrode 15 prior to the mask elimination process for eliminating the particles. The particles are then eliminated to obtain the structure shown in Fig. 10.

**[0098]** Furthermore, as shown in Fig. 11, the carbon regions may be provided as a metal thin film electrode 15a by having them distributed within the metal thin film electrode. In this case, the mask elimination process for eliminating the particles is performed after the formation process of the insulating layer 13 (Fig. 4) to form the recesses, and the formation process of the carbon region is then performed as the metal thin film electrode formation process in which the metal thin film electrode 15a containing the carbon region is formed over the insulating layer 13, with carbon or a carbon compound being mixed with the metal by using i.e. a mixed target or by sputtering within a carbon or carbon compound gas atmosphere. In this way, the structure shown in Fig. 11 is obtained.

**[0099]** Alternatively, the carbon region 40 may be

formed as a thin film deposited under the metal thin film electrode 15 within the island regions 14 as shown in Fig. 12. In this case, the forming process of the carbon region is performed after the formation process of the insulating layer 13 (Fig. 4), followed by the formation process of the metal thin film electrode 15 over the carbon region 40. In this way, the formation process of the carbon region is performed immediately before the formation process of the metal thin film electrode 15. The particles are then eliminated to obtain the structure shown in Fig. 12, in which the carbon region 40 is formed between the metal thin film electrode 15 and the insulating layer 13.

**[0100]** Furthermore, the carbon region 40 may also be a thin film formed between the electron source layer 12 and the insulating layer 13 as shown in Fig. 13. In this case, the carbon region 40 is evenly formed over the electron source layer 12 after the formation of the electron source layer (Fig. 2), the particles 20 are dispersed over the carbon region 40, and the processes from the insulating layer formation (Fig. 4) to the metal thin film electrode formation (Fig. 6) are then performed. In this way, the carbon region formation process is performed immediately before the dispersion process. Thereafter, the particles are eliminated to obtain the structure shown in Fig. 13, in which the carbon region 40 is formed under the insulating layer 13.

**[0101]** In still another embodiment, the carbon region 40 may be formed as a thin film having a thickness which is gradually reduced along with the insulating layer 13 and the metal thin film electrode 15 within the recessed island regions 14. In this case, the carbon region 40 is formed over the electron source layer 12 and the fine particles 20 after the dispersion process (Fig. 3) of the fine particles 20 on the electron source layer 13, and the processes from the insulating layer formation (Fig. 4) to the metal thin film electrode formation (Fig. 6) are then performed. In this way, the carbon region formation process is performed immediately before the insulating layer formation process. Thereafter, the fine particles are eliminated to obtain the structure shown in Fig. 14, in which the carbon region 40 constituted by the carbon thin film having a film thickness which is gradually reduced within the island regions 14, is formed under the insulating layer 13.

**[0102]** As explained heretofore, in the above embodiments, each of the island regions 14 is formed so as to constitute a recess on the flat surface of the metal thin film electrode 15 and the insulating layer 13 through the elimination of a particle 20, however, an electron-emitting device in which the particles 20 are not eliminated is also possible. For instance, by omitting the final particle elimination process shown in Fig. 6, it is possible to manufacture electron-emitting devices retaining the particles as shown in Figs. 15 through 18 which respectively correspond to the electron-emitting devices shown Figs. 10 and 12 through 14. The diameter of a fine particle should be a size which allows a part of the



particle geometry to be exposed, in other words, a size which does not cause the particle to be completely buried. When the thickness of the insulating layer is so large to an extent that the presence of the particles cannot be observed, then the emission current decreases.

[0103] Moreover, although the island regions 14 in the above embodiments are explained as being crater-like recesses 14 caused by the fine particles, the shape of the island regions is not limited to this particular shape, and the island regions may be formed as trench-like recesses 14a as shown in Fig. 21, or cone-shaped recesses 14b as shown in Fig. 24. Any arbitrary shapes (i.e. a rectangular shape) and formation methods are applicable for the island regions.

[0104] The formation processes of the embodiments shown in Figs. 21 and 24 are identical to the above-explained formation processes for the island regions, except that dot-type or line-type tapered blocks 21a and column-type reverse-tapered blocks 21b shown in Figs. 19 and 22 are respectively used in place of the particles. Also, in either of the electron-emitting devices shown in Figs. 21 and 24, a preliminary insulating layer may be provided, and the reverse-tapered blocks 21a, 21b may be formed thereon in the same manner as the case shown in Fig. 8 in which the preliminary insulating layer 13b is provided on the electron source layer 12.

[0105] The reverse-tapered blocks 21a and 21b are made of an electrically insulating material such as a resist. They project out in a direction normal to the substrate 10, and in their top portions, include overhangs 22a and 22b which project in a direction parallel to the substrate 10.

[0106] For the resist as the reverse-tapered block material, a novolac-type photo resist may be used. A spin-coat method is used for the application of the resist. After applying the resist to the electron source layer 12 using a photo mask, the pre-bake, exposure, post-bake and development processes are performed to form a desired resist pattern on the electron source layer. At this point, the pattern may have any arbitrary shape, but it should have a sufficient height from the Si electron source layer so that it would not be completely buried within the insulating layer which is to be formed later. The reverse-tapered block is the one having a cross-section of a reverse-tapered shape, however, the taper angle is arbitrary, and even the blocks are not necessarily be tapered blocks.

[0107] After forming the reverse-tapered resist pattern, the insulating layer 13 and the metal thin film electrode 15 are deposited so as to form island regions 14a and 14b in which the film thicknesses are gradually reduced, thereby forming the substrates shown in Figs. 19 and 22. The reverse-tapered blocks 21a and 21b are eliminated by given agents respectively to obtain substrates, each of which having a plurality of island regions constituting the recesses shown in Figs. 20 and 23. Thereafter, carbon regions 40 made of carbon or a carbon compound is formed over the electron source layer

12, insulating layer 13 and metal thin film electrode 15 in the same manner as in the previous embodiments, thus an electron-emitting device is constituted. It is also possible to form an electron-emitting device in which the reverse-tapered blocks 21a or 21b are not eliminated, but retained instead at the centers of these recesses formed by the island regions as shown in Fig. 19 or 22, and the carbon regions 40 are formed thereon.

#### {Electro-conductive Path Growth in Electron-Emitting Device}

[0108] In addition, although it is optional, after the metal thin film electrode formation process or after the fine particle elimination process, regardless of the presence or absence of the particles, an electroconductive path growing process may be performed over the device substrate 10 having the island regions 14 in which the metal thin film electrode 15 is gradually reduced in thickness and terminated on the insulating layer 13. During this electroconductive path growing process, a voltage is applied between the metal thin film electrode 15 and the electron source layer to cause a given level of current.

[0109] When the electron source layer 12 is formed of Si, the insulating layer or the carbon region located between the edge B of the insulating layer 13 and the edge A of the metal thin film electrode 15 forms a so-called "site" which serves as a current path although it has a high resistivity, so that the current first start flowing through these sites. Upon this, Joule heat is generated and the growth of electroconductive paths is promoted on the surface or the inside of the insulating layer.

[0110] Next, although Si of the electron source layer initially has a high resistivity, the electric resistance of the portions of Si located directly under these sites is locally and selectively reduced, and the amount of current in these portions increases. In this way, the electroconductive paths are collectively and uniformly grown in a shape of ring. By adding the electroconductive paths growing process in this way, undesired electrical breakdown may be avoided since Si initially has a high resistivity. This process also contributes to the stabilization of emission current of the device so obtained.

[0111] In an electron-emitting device according to the embodiments of the present invention, the island regions 14 are formed, in which the insulating layer is gradually thinned locally (in other words, the film thickness thereof is gradually reduced), and the edges of the metal thin film electrode 15 are located on the portions of the insulating layer 13 at which the film thickness is gradually reduced, and these are covered by the carbon region to form the electron-emission sections, so that even with this electron-emitting device, a sufficient level of electron emission can be obtained. However, the electroconductive path growing process may further be performed. By this process, the electroconductive minute structures present on the surface or inside of the

insulating layer or carbon regions within the electron-emitting sections are grown or increased. When such a device is operated, strong fields are centered at these minute structures, and the emission is assumed to be promoted by these structures serving as emission sites. Since the electron-emitting sections having an even size, shape and condition may be formed uniformly over the entire surface of the device with the use of evenly sized and shaped particles etc., a favorable electron emission pattern may be obtained.

[0112] As for the electron emission efficiency, since only the island regions 14 in the surface of the device are functioning as the electron-emitting sources and also as the electroconductive paths, it is believed that electron emission with extremely high efficiency with no leak current can be obtained.

#### {Light-Emitting Device Using Electron-Emitting Device}

[0113] In a case where the electron-emitting device S is used as a light-emitting device, the device substrate 10 of the electron-emitting device S is used as the first substrate on the backside, and a translucent second substrate 1 such as one made of glass is retained as the front side substrate via a vacuumed space 4 as shown in Fig. 1. On the interior surface of the second substrate 1, a translucent collector electrode 2 made of indium-tin oxide (also referred to as ITO), tin oxide (SnO) or zinc oxide (ZnO) etc. and a phosphor layer 3R, G, B are formed. As the material of the device substrate 10, a ceramic such as  $Al_2O_3$ ,  $Si_3N_4$  or BN etc. may also be used as well as glass.

[0114] As shown in Fig. 1, the electron-emitting device is a diode with the metal thin film electrode 15 at the surface as the positive potential  $V_d$  and the ohmic electrode 11 at the backside as the ground potential. When a voltage  $V_d$  of, for example, approximately 50 V is applied between the ohmic electrode 11 and the metal thin film electrode 15 to inject electrons into the electron source layer 12, within an island region 14, the electrons travel from the edge B toward the edge A through the insulating layer 13 or the carbon region 40. The electrons reaching the metal thin film electrode 15 within the island region 14 are assumed to be partially emitted into the vacuum by a strong electric field  $V_c$  through the metal thin film electrode 15 and the adjacent carbon region 40.

[0115] The electrons  $e$  (emission current  $I_e$ ) emitted from the recess formed by the island regions 14 are accelerated by the high acceleration voltage  $V_c$  (i.e. approximately 5 kV) applied to the opposing collector electrode (transparent electrode) 2, and collected to the collector electrode 2. In a case where the phosphor 3 is coated on the collector electrode, a corresponding visible light is emitted.

[0116] Electron-emitting devices according to the present invention were fabricated using an electron source layer made of boron (B)-doped Si, and charac-

teristics are examined.

#### {Embodiment 1}

5 [0117] First, a flat glass substrate for the use as the backside substrate was cleaned and sufficiently dried, and on its one surface, an ohmic electrode of TiN was formed to a thickness of 220 nm through reactive sputtering with the introduction of nitrogen. Thereon, an  
10 electron source layer of Si to which B was doped to 0.15atm% was formed in a thickness of 500 nm. In this way a plurality of electron source layer substrates were fabricated.

[0118] As shown in Fig. 5, by spraying fine particles on the electron source layer of the electron source layer substrates, particle-sprayed substrates were fabricated. In this embodiment, spherical particles (also simply referred to as spacers) having a diameter of  $1.0\mu m$  were used. The material of the particles was  $SiO_2$ , and the  
20 distribution range of the diameters of the particles was extremely small. For the dispersion of the particles, a known spacer spray method employed in the fabrication of liquid crystal display apparatus was used. Among the available methods including dry-spray type methods  
25 and wet-spray type methods, a wet-spray method was used for this device.

[0119] The spherical particles were dispersed within ethyl alcohol and sufficiently stirred so that they do not cling together. This spray solution was then applied to the electron source layer of Si by spin coating, and ethyl alcohol was eliminated. In this way, the spherical particles were homogeneously coated on the Si electron source layer. The distribution density of the particles on the Si electron source layer was approximately 1000  
30 pieces/ $mm^2$ . In this way, a plurality of such particle-sprayed substrates, each having particles within recesses formed by island regions, were formed.

[0120] An insulating layer of  $SiO_2$  was formed in a thickness of 330 nm via reactive sputtering with the introduction of oxygen. At this point, the particles were exposed on the surface.  $SiO_2$  was of course formed also on the surfaces of the particles. The sections near the contact points (grain boundaries) between the particles and the electron source layer were the areas under the overhang portions, so that the layer in that sections was deposited by "go-around" of the sputtering gas, and the film thickness of the insulating layer was gradually reduced toward the contact points.  
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[0121] Thereafter, a mask having a pattern of a metal thin film electrode was placed on the  $SiO_2$  insulating layer, and the metal thin film electrode made of Pt or Au was formed in a thickness of 10 nm. In this way a plurality of device substrates of the electron-emitting devices were provided. At this time, although a surface treatment for the insulating layer is not required prior to the formation of the metal thin film electrode, sputter etching may be performed over the insulating layer before forming the metal thin film electrode. It is preferred because  
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the interfaces between the particles and the insulating layer would be etched and reformed by this sputter etch, and this would allow the electrode material to go around more effectively to reach the interfaces during the metal thin film formation, so that more effective electron emission can be promoted. When the sputter etch was performed, ring-like trails reflecting the shape of the particles were remained on the device surface. For all devices of the present embodiment, the sputter etching was performed prior to the formation of the top metal thin film electrode.

[0122] The sprayed particles were then eliminated from the substrates, thereby forming a plurality of the device substrates as the electron-emitting devices having the recessed island regions without the particles. The elimination of the particles from the electron-emitting device substrates was performed through ultrasonic cleaning with the use of isopropyl alcohol. As for the cleaning solution, water, acetone, ethanol or methanol may also be used.

[0123] The substrates on which a plurality of recessed island regions were provided by the elimination of the particles, were placed within a vacuum chamber, and a voltage is applied between the electron source layer 12 and the metal thin film electrode 15 on the substrate via the ohmic electrode under an atmosphere vacuummed to  $2 \times 10^{-4}$  Torr in conjunction with the introduction of methane gas. The application of voltage was repeated for 1 to 15 cycles intermittently, with one cycle being a rise and a fall of the applied voltage between 0 V and 35 V by a one volt step in every 3 seconds. By changing the cycles and the duration, methane was adsorbed or deposited on, or reacted with the island regions of the devices, forming thin films having thicknesses varying from 0 nm to 50 nm.

[0124] As for comparative samples, a plurality of device substrates of the electron-emitting devices were fabricated in the same manner as the above embodiment, but without the introduction of methane gas into the vacuum chamber, in other words, the thin film constituting the carbon region had not been provided. For these comparative samples, only the same voltage as that in the embodiment was applied between the electron source layers and the metal thin film electrodes, so as to perform electrification repeatedly.

[0125] Separately, a transparent substrate 1 made of transparent glass having an ITO collector electrode and phosphor layer provided on its interior surface were fabricated.

[0126] The above various device substrates and the transparent substrate were placed in parallel, facing each other, with the use of spacers so that they are held at 5mm apart, and the space therebetween was vacuummed to  $10^{-7}$  Torr or  $10^{-5}$  Pa to have the electron-emission devices constitute light-emitting elements.

[0127] For the each of the fabricated light-emitting elements made of the electron-emitting devices and those comparative samples, the diode current  $I_d$ , emission

current  $I_e$  and the efficiency ( $I_e/I_d$ ) were measured while 35 V was being applied as the device voltage  $V_{ps}$  between the metal thin film electrode and ohmic electrode. The results of these measurements are shown in Figs. 25 and 26. In these graphs, the horizontal axis represents the repetition cycles of the carbon-type film formation process or electrification process, and the vertical axis represents the amount of current and efficiency. [0128] As can be seen from Figs. 25 and 26, those electron-emitting devices according to the preferred embodiment having the recessed island regions covered by the carbon films have characteristics higher than the comparative samples by two orders with regard to the emission current  $I_e$ , so that the amounts of the emission current are significantly increased. In this first embodiment, the devices capable of providing emission current over  $2 \times 10^{-2}$  A/cm<sup>2</sup> and emission efficiency over 2% were obtained.

20 {Embodiment 2}

[0129] Through a sputtering method using a carbon target, carbon films were deposited in various thicknesses from 0 nm to 50 nm on respective substrates that had been fabricated in the same manner as the first embodiment (substrates having a plurality of recessed island regions through the elimination of the fine particles). As comparative samples, a plurality of device substrates of electron-emitting devices were fabricated by the same processes as the above embodiment except that the thin films forming the carbon regions were not provided to those samples. Unlike the first embodiment, the voltage application was not performed in this embodiment during the deposition of the carbon.

[0130] In the same way as the first embodiment, a transparent glass substrate 1 in which an ITO collector electrode and a phosphor layer are formed on its interior surface was fabricated, and it is assembled to form light-emitting elements of the electron-emitting devices.

[0131] Device voltage of 35 V was applied between the metal thin film electrodes and the ohmic electrodes of the fabricated light-emitting elements of the electron-emitting devices and the comparative samples, and measurements were performed for diode current  $I_d$ , emission current  $I_e$  and the efficiency ( $I_e/I_d$ ) of the elements having carbon-type film layers in various thicknesses. Fig. 27 shows the relationship between the thicknesses of the carbon-type film layers, and the amounts of current and efficiency of the respective devices.

[0132] Those electron-emitting devices according to the second embodiment, having recessed island regions covered by carbon-type films by sputtering had characteristics higher than the comparative samples by two orders, indicating significant increase in their emission current. In this second embodiment, devices capable of providing emission current over  $4 \times 10^{-2}$  A/cm<sup>2</sup> and emission efficiencies over 6% were obtained.

[0133] Furthermore, with the condition of either one of the above embodiment, devices were fabricated to have film thicknesses ranging from 50 nm to 1000 nm with the total thicknesses of their insulating layers being 50 nm or greater, and through the application of a voltage 200 V or lower, their emission efficiencies were measured to see the variation in the electron emission efficiencies  $I_e/(I_e+I_d) \times 100\%$  relative to the thicknesses of the insulating layers. The results of the measurements indicated that emission efficiencies of 0.1% or greater can be obtained with devices having film thicknesses between 50 nm and 1000 nm with the thicknesses of their insulating layers being 50 nm or greater.

[0134] Furthermore, it was confirmed that devices having Si electron source layer not doped with B would have the same effect.

#### {Other Structures of The Electron-Emitting Device}

[0135] In the above embodiments, the electron-emitting device was explained as having recesses or trench-like regions, in which the film thickness is gradually reduced toward the center of the island regions 14, however, the device may have island regions in which the insulating layer and metal thin film electrode gradually reduce together off the center of the island regions or gradually reduce asymmetrically, or gradually reduce as a flat section.

[0136] For example, as another embodiment of the present invention, an island region 14 in which the film thicknesses of the insulating layer 13 and the metal thin film electrode 15 gradually reduce together toward a masking wall 20a, may be provided on one side of a trench-like recess as shown in Fig. 28.

[0137] The island region 14 shown in Fig. 28, which constitutes a portion of the trench-like recess, may be formed as follows. First, a masking wall 20a made of a resist etc. is formed in the same manner as the line-type tapered block 21a shown in Fig. 19 on a substrate 10 having an ohmic electrode 11 and an electron source layer 12 sequentially formed thereon. An insulating layer 13 is then formed by sputtering. In this sputtering process, the surface of the electron source layer 12 on the substrate 10 is so disposed to have a tilt relative to the flowing direction of the sputtered insulating material, so that the resultant insulating layer 13 would have, on one side of the masking wall 20a, a portion in which a smaller amount of insulating material is deposited, or a portion in which the thickness of the insulating layer is gradually reduced toward the masking wall 20a. Then in the next process, the surface of the insulating layer 13 on the substrate 10 is so disposed to have a tilt relative to the flowing direction of a sputtered metal thin film electrode material, so that the resultant metal thin film electrode 15 would have, on one side of the masking wall 20a, a portion in which a smaller amount of the metal thin film electrode material is deposited, or a portion in which the thickness of the metal thin film electrode is gradually re-

duced.

[0138] As indicated in Fig. 28, in these "tilted" sputtering processes of the insulating layer and metal thin film electrode, when angles of the substrate 10 in a sputtering apparatus are selected so as to allow the incidence angle  $\theta'$  of the flow of the sputtered metal thin film electrode material to be larger than the incidence angle  $\theta$  of the flow of the sputtered insulating material, the resultant island region 14 would then have a structure in which the metal thin film electrode 15 terminates at an edge A located on the insulating layer 13. The insulating layer 13 within the island region 14 terminates at an edge B located on the electron source layer 12.

[0139] Thereafter, over the exposed portions of the masking wall 20a and insulating layer 13, and over the metal thin film electrode 15, a carbon region 40 made of carbon or a carbon compound is formed by sputtering a carbon or carbon compound material in the similar manner as the above processes, thereby completing an electron-emitting device as shown in Fig. 29. The masking wall 20a and the deposits thereon may be eliminated through etching etc. to form the carbon region 40 over a structure in which the electron source layer 12 is exposed.

[0140] In the above embodiment, the island region was explained as being formed as a recess, however, the island region may be formed as a flat or convex structure in which the thicknesses of the insulating layer 13 and the metal thin film electrode 15 are gradually reduced. For example, as another embodiment, a flat or convex island region 14 in which the film thicknesses of the insulating layer 13 and the metal thin film electrode 15 are gradually reduced toward a peak of a spiked portion 12a of the electron source layer 12 as shown in Fig. 30. This flat or convex island region 14 is formed by techniques such as photolithography and etching etc. with the use of a dot-type or linear mask. The spiked portions 12a of the electron source layer 12 may be formed as ribs as shown in Fig. 30, or individual convex features distributed over the surface as shown in Fig. 31. In these cases also, the metal thin film electrode 15 terminates at the edge A located on the insulating layer 13, and the insulating layer 13 terminates at the edge B located on the electron source layer 12. It is also possible to form a device having a structure in which the electron source layer 12 is completely covered by laying the insulating layer 13 over the peaks 12a of the electron source layer 12.

[0141] Thereafter, as shown in Fig. 32, a carbon region 40 made of carbon or a carbon compound is formed over the exposed portions of the insulating layer 13 and electron source layer 12 and the metal thin film electrode 15 in the same manner as the prior embodiments, thereby completing an electron-emitting device.

{Display Apparatus Using The Electron-Emitting Devices}

[0142] Fig. 33 shows a flat panel display apparatus using the electron-emitting devices according to an embodiment of the present invention. Fig. 34 shows a partial cross-sectional view of the flat panel display apparatus.

[0143] On the interior surface (on the side of a vacuumed space 4) of a backside substrate 10, a plurality of ohmic electrodes 11 extending in parallel to each other are formed. Every 3 ohmic electrodes 11 are grouped as one set, corresponding to red, green and blue color signals R, G and B so as to allow the apparatus to function as a color display panel, so that a specific signal is applied to each of them. A plurality of electron-emitting devices S are arranged along a common ohmic electrode 11. A plurality of bus lines 16 extend in parallel to each other, each of them being formed on the portions of the carbon regions 40 provided on metal thin film electrodes 15 of adjoining devices for providing electrical connections, and is vertically extended to ohmic electrodes 11. An intersection of a bus electrode 16 and an ohmic electrode 11 corresponds to an electron-emitting device S. Accordingly, either the simple matrix method of active matrix method is applicable as the driving method of the display apparatus of the invention.

[0144] An electron-emitting device S, as shown in Fig. 34, comprises an electron source layer 12, an insulating layer 13, a metal thin film electrode 15 and a carbon region 40 covering island regions, that are sequentially formed on the ohmic electrode 11. The carbon region 40 may face to the interior vacuumed space as shown in Fig. 1. The device has a plurality of island regions homogeneously distributed, in which the film thicknesses are gradually reduced in the same direction as the interface with the carbon region 40 such as those shown in Figs. 10-18, 21, 24, 29 and 32. In Figs. 33 and 34, the indication of the plurality of island regions as recesses are omitted, however, it is assumed herein that a plurality of island regions have been provided homogeneously in the insulating layer 13 and the metal thin film electrode 15.

[0145] It should be noted that insulating supporting members 17 are provided, encompassing individual electron-emitting devices S to segregate them into a plurality of electron-emitting regions. The insulating supporting members 17 support the bus electrodes 16, and prevent them from being broken. That is, the supporting members 17 should be formed beforehand with a material having a high insulation performance or electric resistance on the periphery sections outside the areas where the electron-emitting devices S are to be formed. These supporting members 17 should be formed in a thickness approximately equal to the final thickness of the electron-emitting devices that are formed in the subsequent processes.

[0146] In this embodiment, partition walls RR on the

backside substrate are formed on the insulating supporting members 17, so as to project out from the backside substrate 10 into the vacuumed space 4. The partition walls RR are disposed at a given interval. In Fig. 33, one partition wall RR is formed for every column of the electron-emitting devices S at a location between two adjacent columns, however, it may be formed intermittently by leaving only a part of its top area including the portions for abutting against a second partition wall FR formed on the front substrate 1.

[0147] Furthermore, it is preferred that the top surface area of this partition wall RR is formed larger than the bottom surface area, in other words, it is preferred to form the partition wall RR so as to include overhangs in its top portion, projecting in a direction substantially parallel to the backside substrate.

[0148] The bus electrodes 16 provided on the metal thin film electrodes 15 on the backside substrate 10 is illustrated as having a simple linear shape in Fig. 33, however, the bus electrodes 16 is preferably formed, instead of in the simple linear shape, in a shape having wider sections on the areas between the metal thin film electrodes 15 of electron-emitting devices and narrower sections on the metal thin film electrodes 15, in other words, a larger width over the areas between electron-emitting devices and a smaller width over the devices. In this way, the resistivity of the bus electrodes may be reduced.

[0149] As for the materials of the ohmic electrode 11, a material typically used in IC wiring, such as Au, Pt, Al, W or the like may be used, but a 3-layer structure of chrome-nickel-chrome, an alloy of Al and Nd, an alloy of Al and Mo or an alloy of Ti and N may also be used as well. Its thickness should be uniform to be able to supply substantially the same amount of current. Although it is not particularly indicated in Fig. 33, an insulating layer made of an insulator such as  $\text{SiO}_x$ ,  $\text{SiN}_x$ ,  $\text{Al}_2\text{O}_3$ , AlN or the like may be provided between the backside substrate 10 and the ohmic electrodes 11. The insulating layer serves to prevent the adverse effect from the backside glass substrate 10 (elution of impurities to alkaline components etc. or unevenness in the substrate surface) over the devices.

[0150] The material of the metal thin film electrode 15 is preferably one having a small work function  $\phi$  in light of the principle of the electron emission. To maximize the electron emission efficiency, the material of the metal thin film electrode 15 is preferably selected from the group I and II metals, for example, Cs, Rb, Li, Sr, Mg, Ba and Ca etc., or an alloy thereof. Furthermore, a chemically stable metal having high electroconductivity is also preferable as the material of the metal thin film electrode 15. For example, Au, Pt, Lu, Ag or Cu, or an alloy thereof is desirable. These metals coated or doped with any of the above metal explained as having a small work function are similarly effective.

[0151] As for the material of the bus electrode 16, a material typically used for IC wiring, such as Au Pt, Al,

Cu or the like may be used. A sufficient thickness should be selected for providing substantially the same amount of current to each of the devices, and the adequate thickness is between 0.1 $\mu$ m and 50 $\mu$ m. However, if the resistivity is tolerable, the material used for the metal thin film electrode may be used instead of the bus electrode.

[0152] On the interior surface (surface facing the backside substrate 10) of the translucent front substrate 1, such as a transparent glass substrate, serving as a display screen, a transparent collector electrode 2 is formed integrally, and a high voltage is applied thereto. When using black stripes or a back metal, it may be utilized as the collector electrode, so that in this case, the formation of ITO is unnecessary.

[0153] Over the collector electrode 2, front ribs (second partition walls) FR are formed in plurality in parallel to the ohmic electrodes 11. Over the collector electrode 2 between the elongated front ribs, phosphor layers 3R, 3G and 3B made of respective phosphors corresponding to R, G and B are respectively formed so as to face to the vacuumed space 4. In this way, at the boundaries between each of the phosphor layers, the front ribs (second partition walls) FR for retaining a constant spacing (i.e. 1mm) between the backside substrate and the front substrate are provided, thereby ensuring the segregation of the phosphors on the front-side substrate that correspond to the three primary colors of light R, G and B respectively.

[0154] As explained in the above, the flat panel display apparatus using the electron-emitting devices according to the present invention has the image display array comprising a plurality of light-emitting pixels, corresponding to the electron-emitting devices, arranged in a matrix, each constituted as either a red R, green G or blue B light-emitting section. It is of course possible also to form a monochrome display panel by replacing the RGB light-emitting sections with monochrome light-emitting sections.

[0155] According to still another embodiment of the present invention, as shown in Fig. 35, an electron-emitting light-emitting device 30 can be obtained. An electron emitting device S as implemented in the electron-emitting light-emitting device 30, is constructed in a similar manner as the above embodiments, in which; an electron source layer 12 is formed on a glass device substrate 10 as the backside substrate having an ohmic electrode 11 formed thereon; a plurality of spherical particles are sprayed, or a plurality of linear or column-like reverse-tapered blocks are formed thereon; an insulating layer 13 and a metal thin film electrode 15 are deposited thereon; the particles etc. are eliminated; and a carbon region 40 made of carbon or a carbon compound is formed over the recessed island regions 14 and the metal thin film electrode 15.

[0156] Over this carbon region 40 of the electron-emitting device according to the present embodiment, a phosphor layer 3 is directly formed, thereby completing the electron-emitting light-emitting device. The

phosphor layer 3 directly receives the electrons generated from the island regions 14 of the electron-emitting device, and emits a visible light corresponding to the type of the phosphor. The electron-emitting light-emitting device 30 may be the one in which the particles 20 (or reverse tapered blocks) are retained, and the carbon region 40 is applied over them as shown in Fig. 36.

[0157] The phosphor layer 3 may be formed by a spin-coating method using a solution of phosphor for the emission of a desired color of light, however the application method is not limited.

[0158] It is also possible to provide over the phosphor layer, a translucent front surface substrate such as a glass substrate having a transparent collector electrode provided on its interior surface, mainly for the protection of the device. This would allow the collection of those electrons leaked from the electron-emitting light-emitting device. These opposing front and the backside substrates of this electron-emitting light-emitting device may be bonded via a transparent adhesive, with the support of spacers etc. at their peripheries.

[0159] According to the configuration of this alternate embodiment of the invention, since the device would have a phosphor layer directly provided on the metal thin film electrode or the carbon region of the electron-emitting device, the application of accelerating power is unnecessary, the driving system of the apparatus may be simplified, and a vacuumed space is no longer required, thus, a light-weight and ultra-thin flat panel display apparatus may be obtained. Furthermore, since it would not require excessive spacers, the visibility may also be improved.

## 35 Claims

### 1. An electron-emitting device comprising:

an electron source layer made of one of a metal, a metal alloy and a semiconductor;  
an insulating layer formed on said electron source layer, said insulating layer having at least one island region serving as an electron-emitting section in which film thickness of said insulating layer is gradually reduced;  
a carbon region made of one of carbon and a carbon compound provided on at least one of top, bottom and inside of said island region; and  
a metal thin film electrode formed on said insulating layer, wherein electrons are emitted upon application of an electric field between said electron source layer and said metal thin film electrode.

### 2. An electron-emitting device as claimed in claim 1 wherein said metal thin film electrode and said carbon region are deposited by one of a physical deposition method and a chemical deposition method.

3. An electron-emitting device as claimed in claim 1 wherein said carbon region is a thin film deposited on one of said island region and said metal thin film electrode.
4. An electron-emitting device as claimed in claim 1 wherein said carbon region is a thin film deposited on said island region while a voltage is being applied between said electron source layer and said metal thin film electrode.
5. An electron-emitting device as claimed in claim 4 wherein said applied voltage is supplied intermittently according to a voltage application period in which the voltage rises and falls.
6. An electron-emitting device as claimed in claim 1 wherein said carbon region is distributed within said metal thin film electrode.
7. An electron-emitting device as claimed in claim 1 wherein said carbon region is a thin film deposited under said metal thin film electrode.
8. An electron-emitting device as claimed in claim 1 wherein said carbon region is a thin film deposited under said insulating layer.
9. An electron-emitting device as claimed in claim 1 wherein the thickness of said metal thin film is gradually reduced in conjunction with said insulating layer.
10. An electron-emitting device as claimed in claim 1 wherein the thickness of said carbon region is gradually reduced in conjunction with said insulating layer.
11. An electron-emitting device as claimed in claim 1 wherein said insulating layer is made of a dielectric material and has a thickness of at least 50 nm in areas other than said island region.
12. An electron-emitting device as claimed in claim 1 wherein said metal thin film electrode terminates on said insulating layer within said island region.
13. An electron-emitting device as claimed in claim 1 wherein said insulating layer terminates on said electron source layer within said island region.
14. An electron-emitting device as claimed in claim 1 wherein said island region is a recess on a flat surface of said metal thin film electrode and said insulating layer.
15. An electron-emitting device as claimed in claim 1 further comprising a fine particle within said island region.
16. An electron-emitting device as claimed in claim 1 further comprising, within said island region, a reverse-tapered block projecting in a direction normal to said substrate and at a top portion thereof, includes an overhang projecting in a direction parallel to said substrate.
17. A method for manufacturing an electron-emitting device comprising:
  - an electron source layer formation process for forming an electron source layer made of one of a metal, a metal alloy and a semiconductor on a substrate;
  - a mask formation process for forming masks on said electron source layer, each of said masks providing a shade around an area in which the masks contact with said electron source layer;
  - an insulating layer formation process for depositing an insulating layer over said electron source layer and said masks so as to provide said insulating layer as a thin film of an insulator, said insulating layer having island regions in which film thickness of said insulating layer is gradually reduced in the proximity of the contact areas of said masks; and
  - a metal thin film electrode formation process for forming a metal thin film electrode over said insulating layer, thereby constituting said island regions as electron-emitting sections;

said manufacturing method further comprising a process for providing a carbon region made of one of carbon and a carbon compound proximal to said island regions.
18. A manufacturing method as claimed in claim 17 further comprising a mask elimination process for eliminating said masks immediately after said metal thin film electrode formation process, and said process for providing said carbon region is performed immediately after said mask elimination process, thereby forming said carbon region as a thin film deposited over said metal thin film electrode.
19. A manufacturing method as claimed in claim 18 wherein said process for providing said carbon region is performed by depositing said carbon region as a thin film while applying a voltage between said electron source layer and said metal thin film electrode.
20. A manufacturing method as claimed in claim 19 wherein said applied voltage is supplied intermittently according to a voltage application period in which the voltage rises and falls.



21. A manufacturing method as claimed in claim 17 further comprising a mask elimination process for eliminating said masks immediately after said insulating layer formation process, and said process for providing said carbon region is performed during said metal thin film electrode formation process, thereby having said carbon region distributed within said metal thin film electrode.
22. A manufacturing method as claimed in claim 17 wherein said process for providing said carbon region is performed immediately after said metal thin film electrode formation process, thereby forming said carbon region as a thin film deposited over said metal thin film electrode.
23. A manufacturing method as claimed in claim 17 wherein said process for providing said carbon region is performed immediately before said metal thin film electrode formation process, thereby forming said carbon region as a thin film deposited under said metal thin film electrode.
24. A manufacturing method as claimed in claim 17 wherein said process for providing said carbon region is performed immediately before said insulating layer formation process, thereby forming said carbon region as a thin film deposited under said insulating layer.
25. A manufacturing method as claimed in claim 22 further comprising a mask elimination process for eliminating said masks immediately after said metal thin film electrode formation process, and said process for providing said carbon region is performed immediately after said mask elimination process, thereby forming said carbon region as a thin film deposited over said metal thin film electrode.
26. A manufacturing method as claimed in claim 17 wherein a voltage is applied between said electron source layer and said metal thin film electrode immediately after at least one of said process for providing said carbon region, said metal thin film electrode formation process, and said mask elimination process.
27. A manufacturing method as claimed in claim 17 wherein said masks are fine particles, and said mask formation process comprises a step of spraying said fine particles onto said electron source layer.
28. A manufacturing method as claimed in claim 17 wherein each of said masks is an electrically insulating reverse-tapered block which projects outwardly in a direction normal to said substrate and has an overhang in a top portion thereof, projecting in a direction parallel to said substrate, and said mask formation process includes steps of:
- forming a reverse-tapered block material layer on said substrate;
- forming thereon a resist mask which allows at least a part of said electron source layer to be exposed through a photolithographic method; and
- etching out said reverse-tapered block having said overhang by one of a dry etching method and a wet etching method.
29. A manufacturing method as claimed in claim 17 wherein said insulating layer, said metal thin film electrode and said carbon region are deposited by a physical deposition method or a chemical deposition method.
30. A display apparatus comprising:
- a first substrate and a second substrate facing each other with a vacuum space therebetween;
- a plurality of electron-emitting devices provided on said first substrate;
- a collector electrode provided on an interior surface of said second substrate; and
- a phosphor layer formed on said collector electrode; wherein each of said electron-emitting devices comprises an electron source layer made of one of a metal, a metal alloy and a semiconductor formed on an ohmic electrode, an insulating layer formed on said electron source layer and a metal thin film electrode formed on said insulating layer, said insulating layer having at least one island region constituting an electron-emitting section in which the film thickness of said insulating layer is gradually reduced, and a carbon region made of one of carbon and a carbon compound is provided on at least one of a top, bottom and inside of said island region.
31. A display apparatus as claimed in claim 30 wherein said insulating layer, said metal thin film electrode and said carbon region are deposited by one of a physical deposition method and a chemical deposition method.
32. A display apparatus as claimed in claim 30 wherein said carbon region is a thin film deposited on one of said island region and said metal thin film electrode.
33. A display apparatus as claimed in claim 30 wherein said carbon region is a thin film deposited on said island region while a voltage is being applied between said electron source layer and said metal thin



film electrode.

34. A display apparatus as claimed in claim 30 wherein said applied voltage is supplied intermittently according to a voltage application period in which the voltage rises and falls. 5
35. A display apparatus as claimed in claim 30 wherein said carbon region is distributed within said metal thin film electrode. 10
36. A display apparatus as claimed in claim 30 wherein said carbon region is a thin film deposited under said metal thin film electrode. 15
37. A display apparatus as claimed in claim 30 wherein said carbon region is a thin film deposited under said insulating layer.
38. A display apparatus as claimed in claim 30 wherein the thickness of said metal thin film electrode is gradually reduced in conjunction with said insulating layer. 20
39. A display apparatus as claimed in claim 30 wherein the thickness of said carbon region is gradually reduced in conjunction with said insulating layer. 25
40. A display apparatus as claimed in claim 30 wherein said insulating layer is made of a dielectric material and has a film thickness of at least 50 nm in areas other than said island region. 30
41. A display apparatus as claimed in claim 30 wherein said metal thin film electrode terminates on said insulating layer within said island region. 35
42. A display apparatus as claimed in claim 30 wherein said insulating layer terminates on said electron source layer within said island region. 40
43. A display apparatus as claimed in claim 30 wherein said island region is a recess on a flat surface of said metal thin film electrode and said insulating layer. 45
44. A display apparatus as claimed in claim 30 further comprising a fine particle within said island region.
45. A display apparatus as claimed in claim 30 further comprising, within said island region, a reverse-tapered block which projects outwardly in a direction normal to said substrate and has an overhang in a top portion thereof, projecting in a direction parallel to said substrate. 50 55
46. A display apparatus as claimed in claim 30 wherein bus lines are formed over a plurality of said metal

thin film electrodes, and said ohmic electrodes and said bus lines are electrodes, each having a shape of a strip, and arranged orthogonal to each other.

Fig. 1

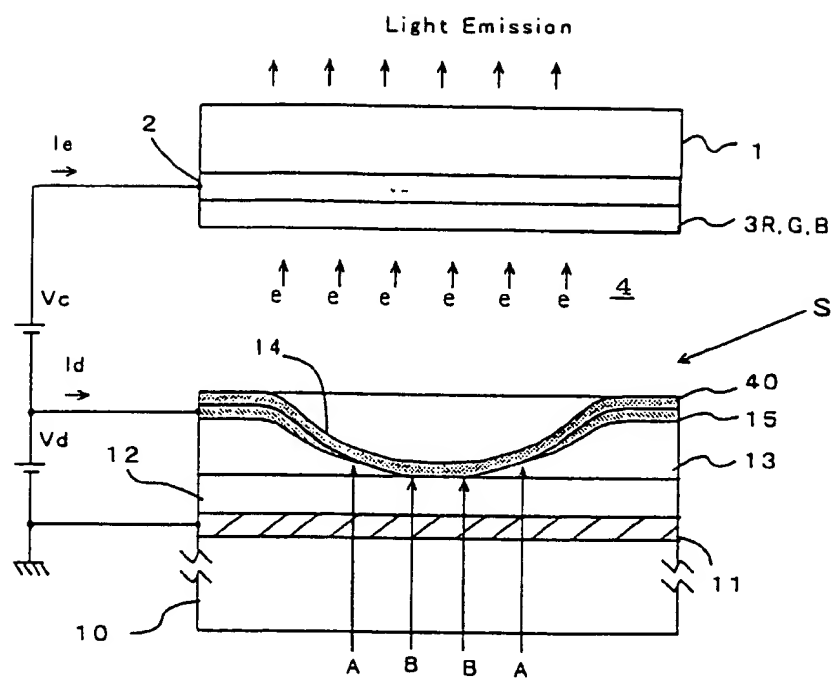


Fig. 2

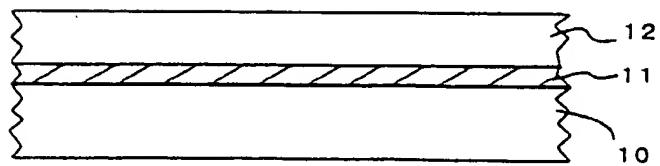


Fig. 3

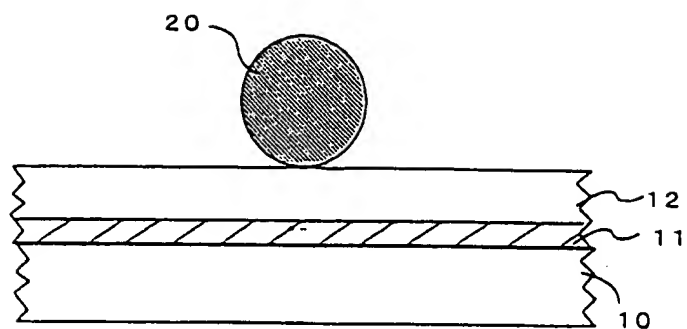


Fig. 4

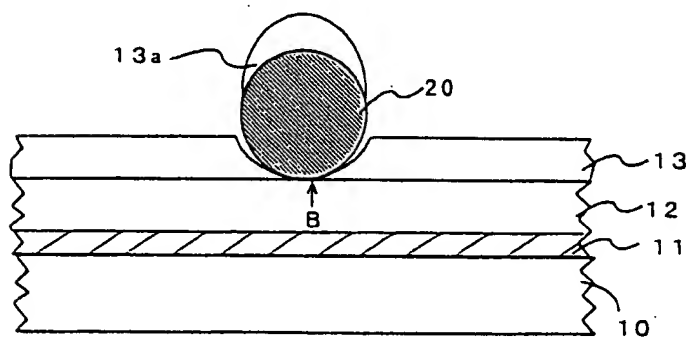


Fig. 5

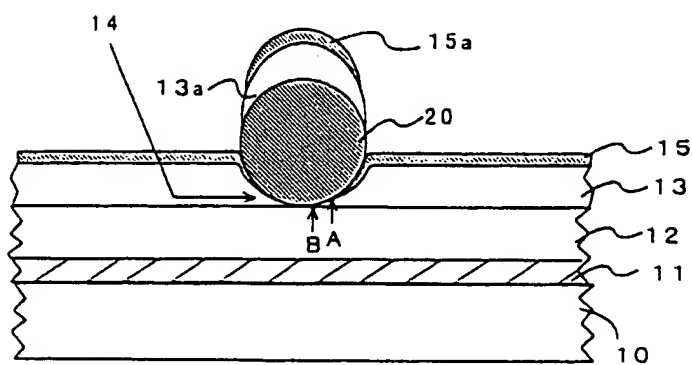


Fig. 6

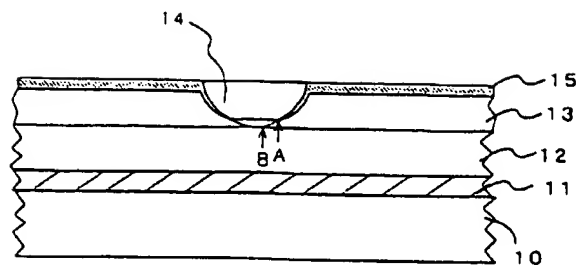


Fig. 7

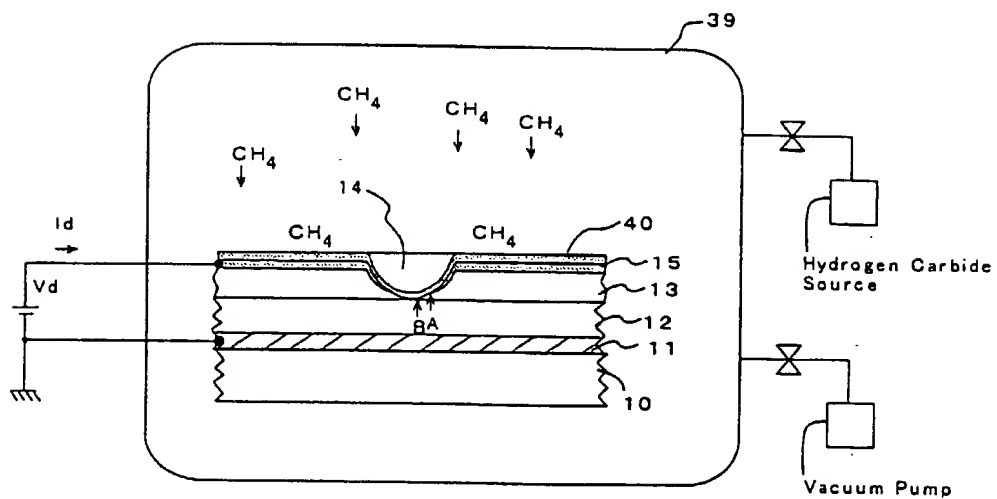


Fig. 8

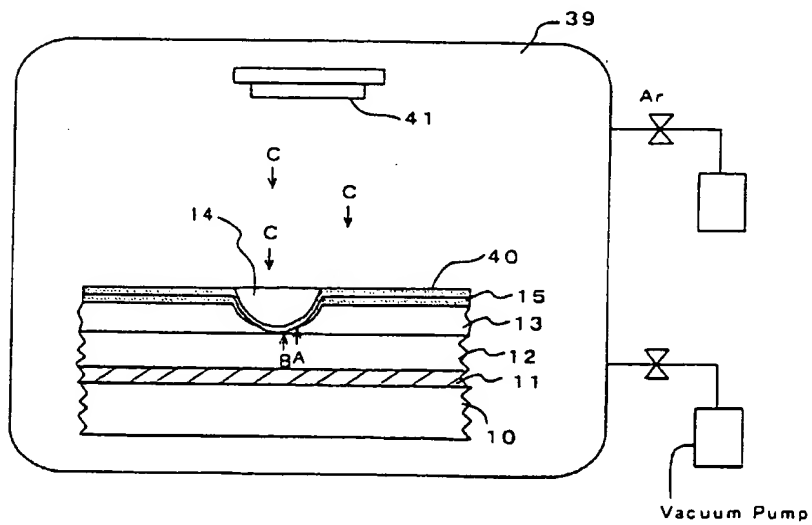


Fig. 9

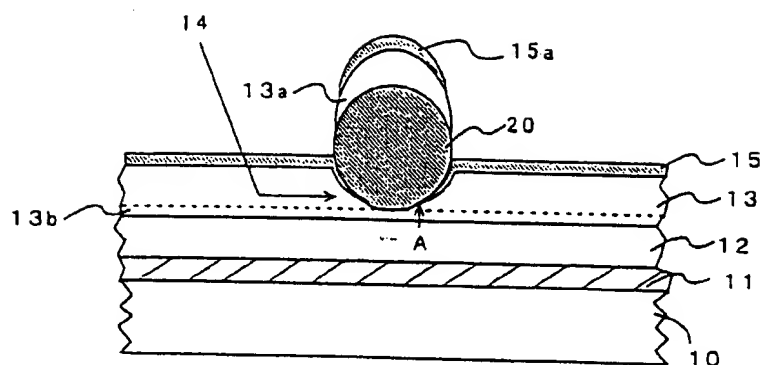


Fig. 10

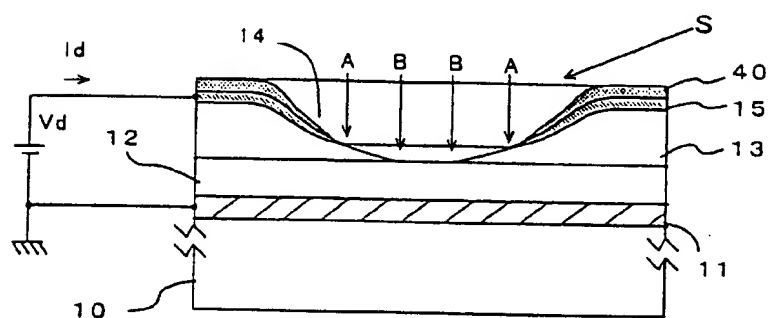


Fig. 11

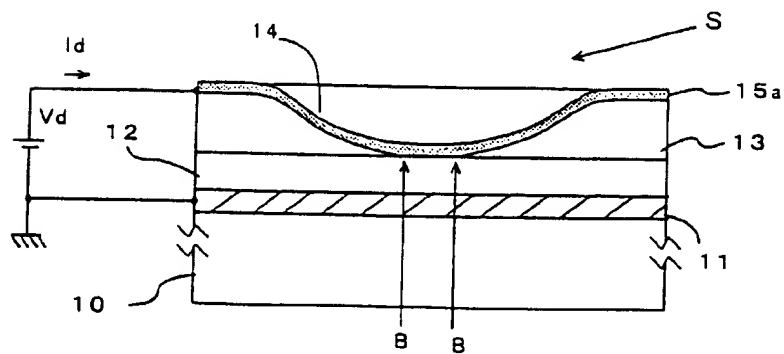


Fig. 12

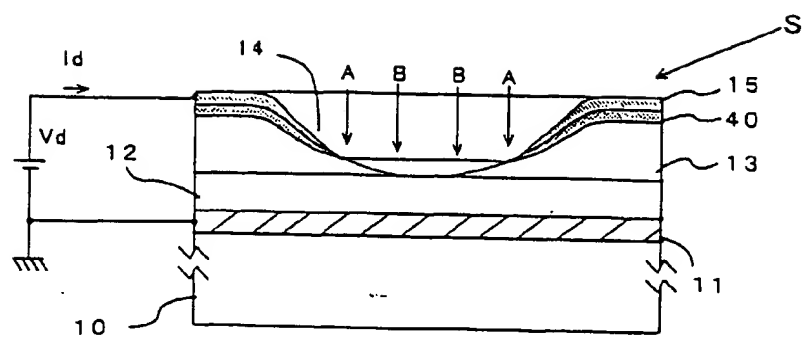


Fig. 13

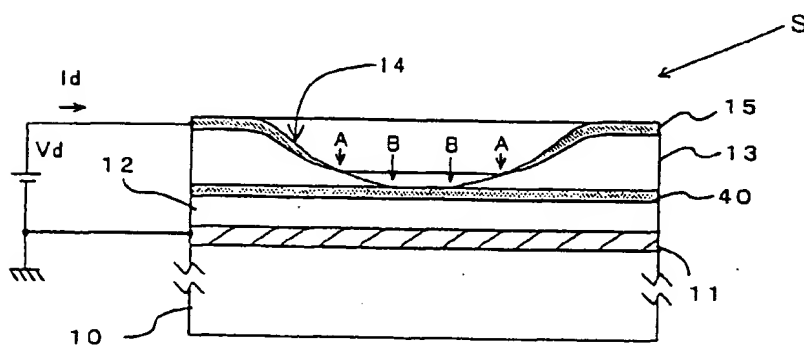


Fig. 14

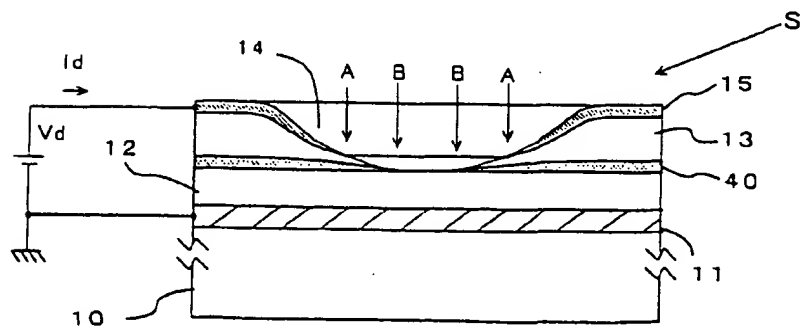


Fig. 15

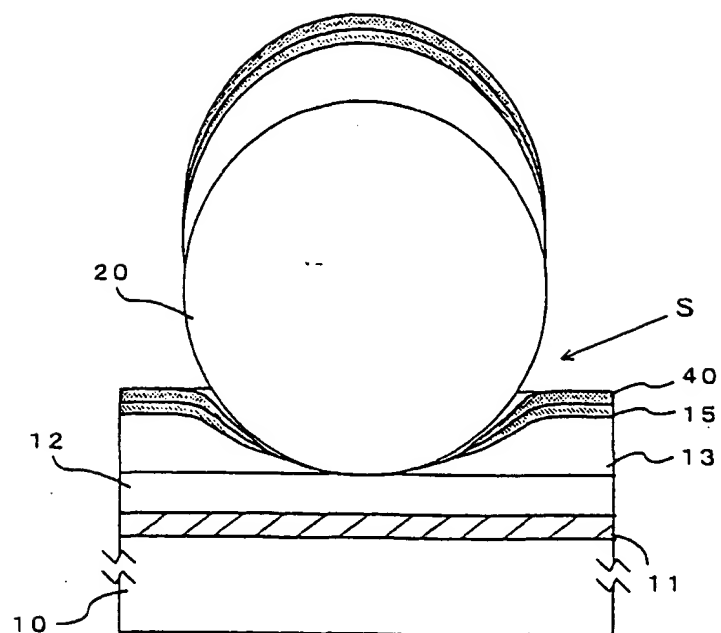


Fig. 16

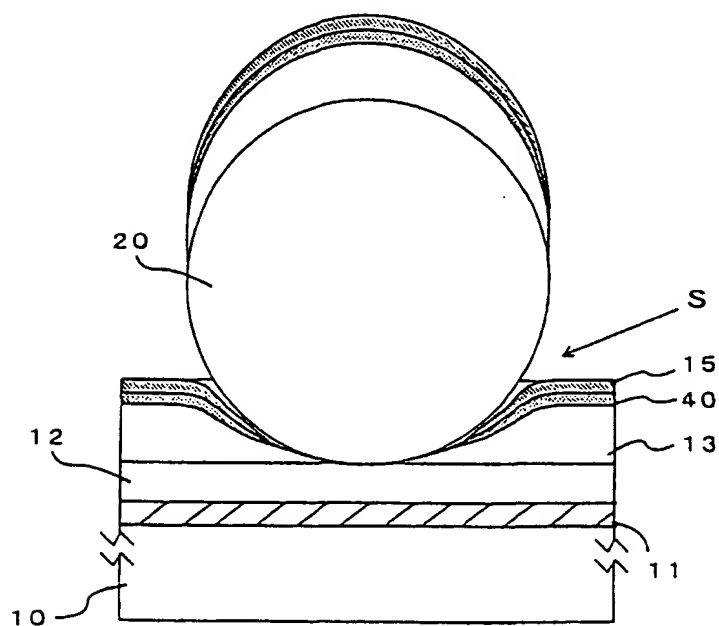


Fig. 17

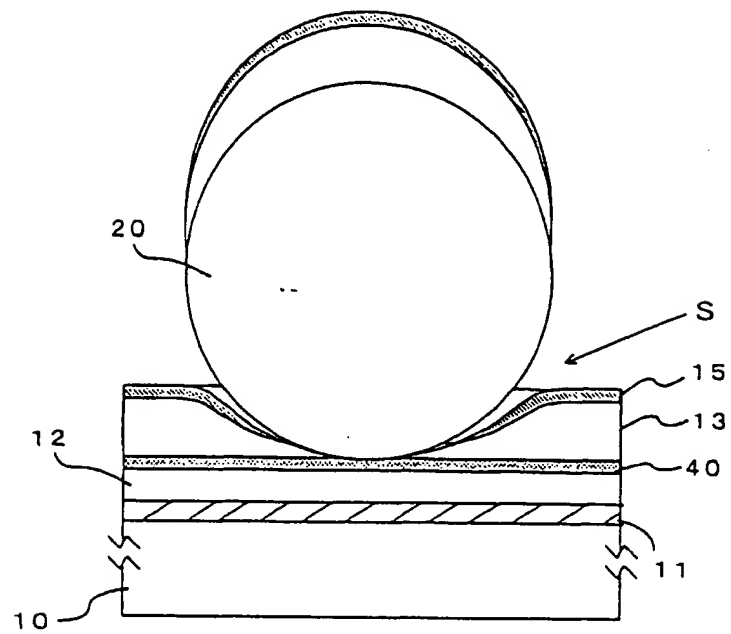


Fig. 18

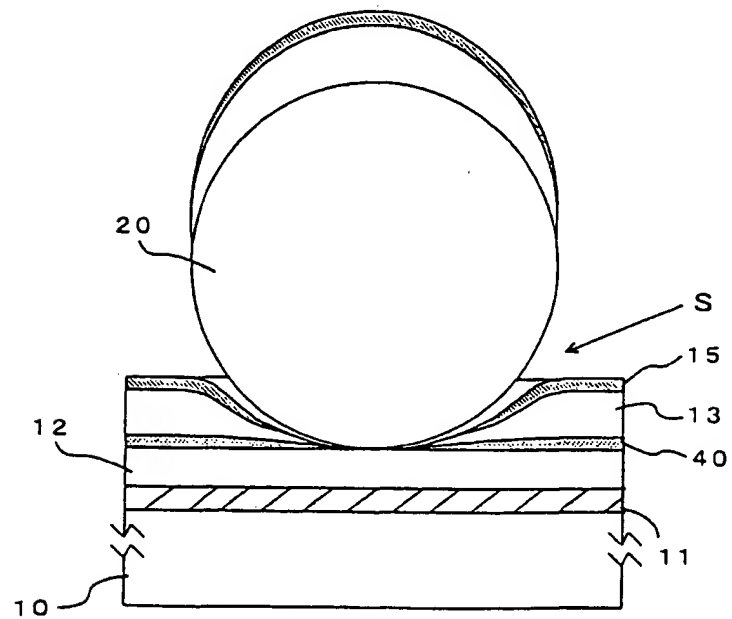




Fig. 19

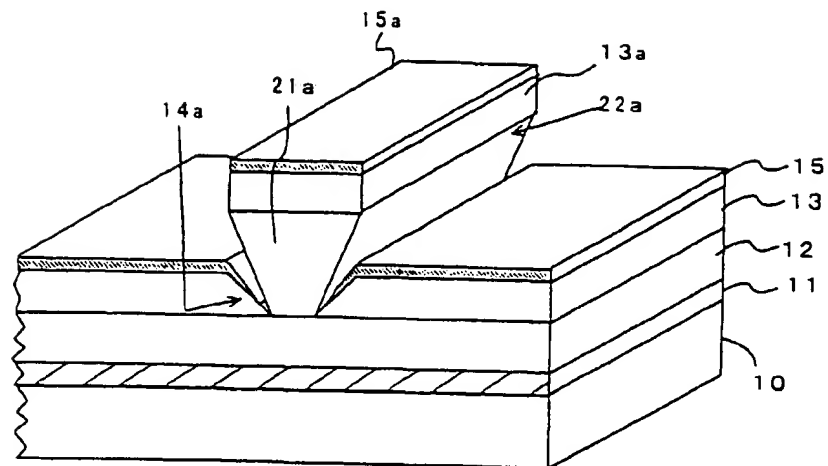


Fig. 20

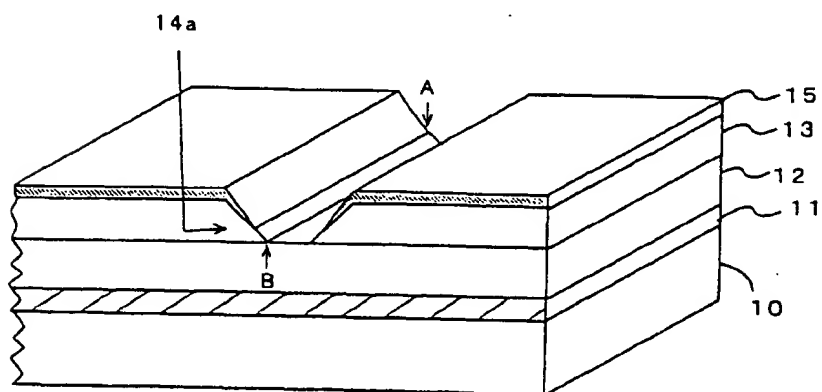


Fig. 21

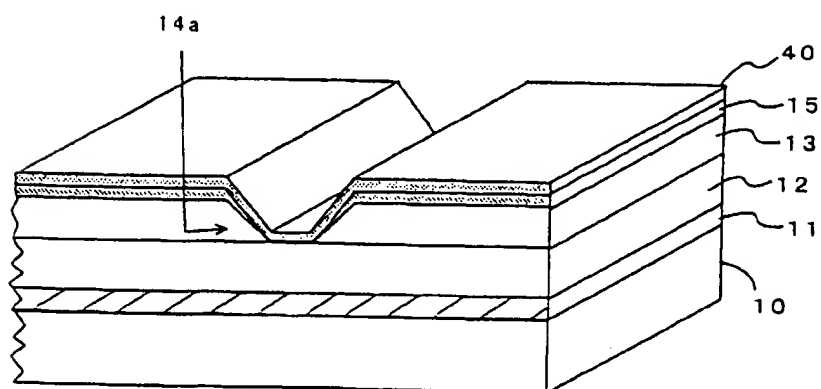


Fig. 22

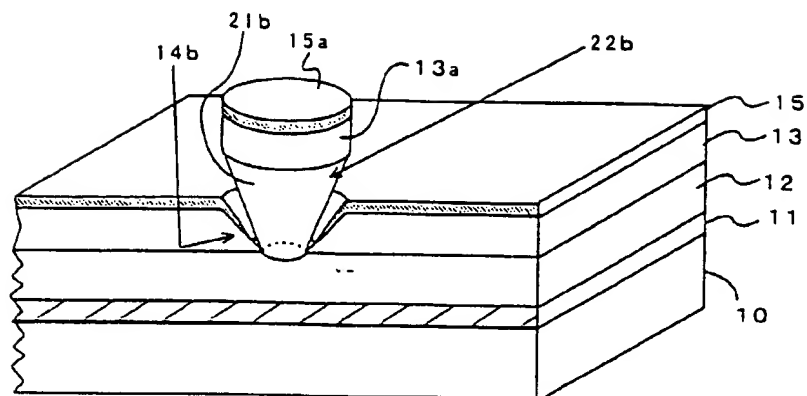


Fig. 23

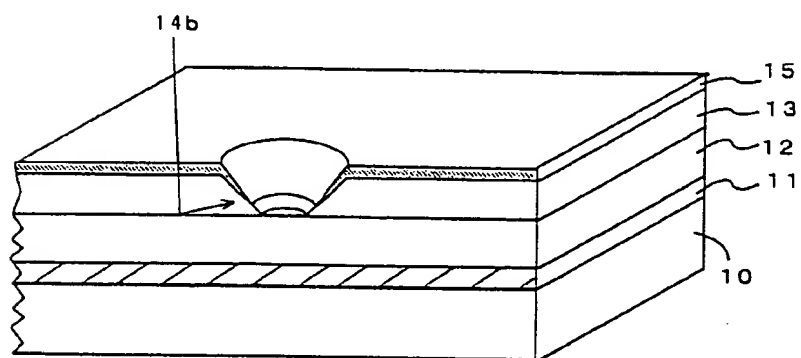


Fig. 24

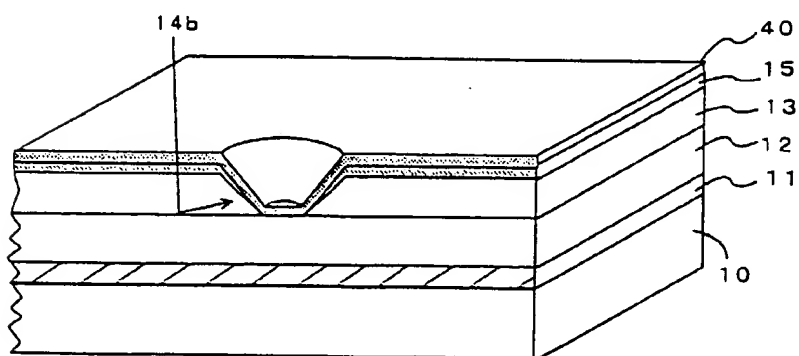


Fig. 25

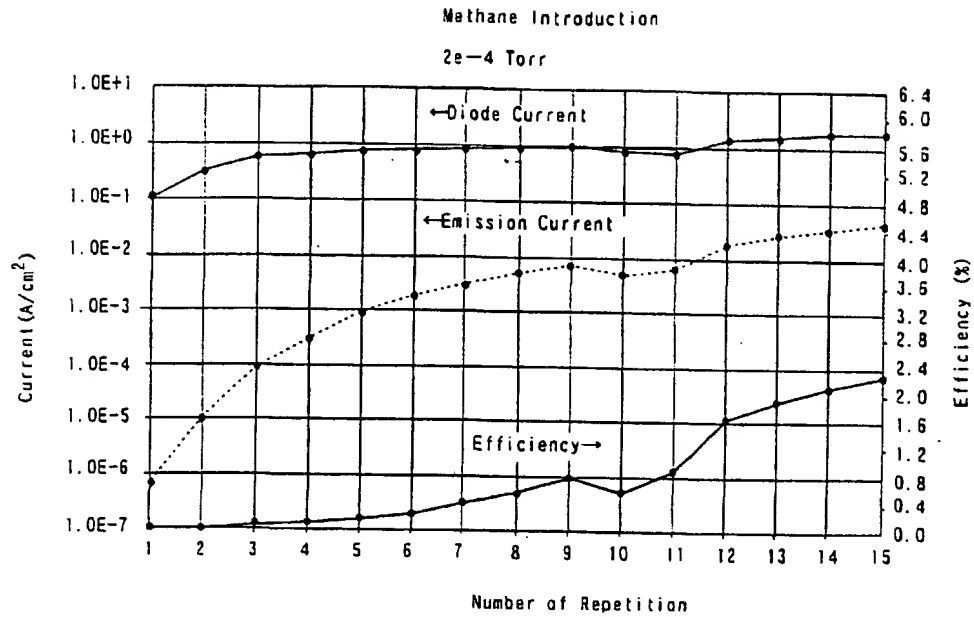


Fig. 26

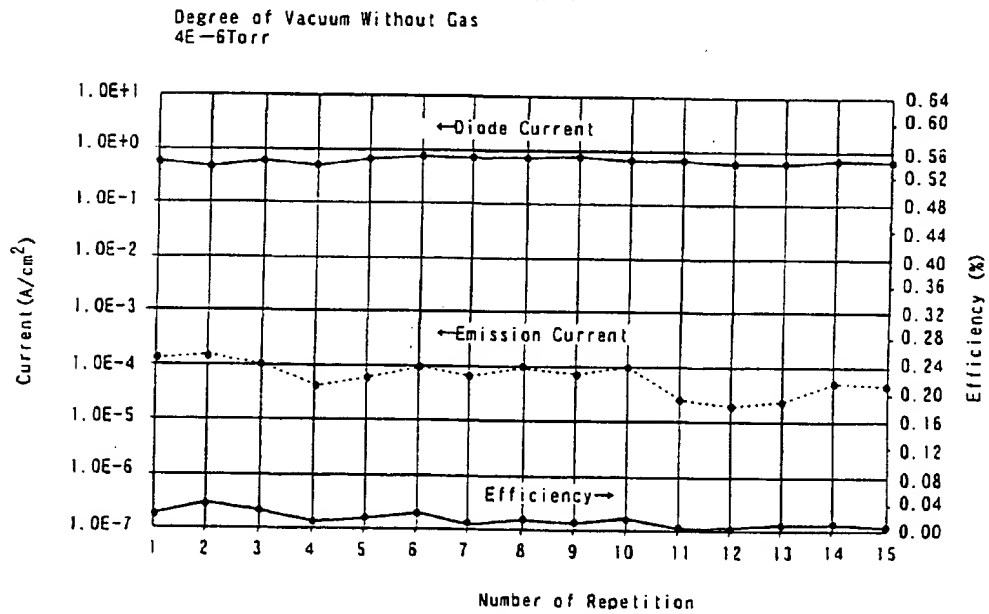


Fig. 27

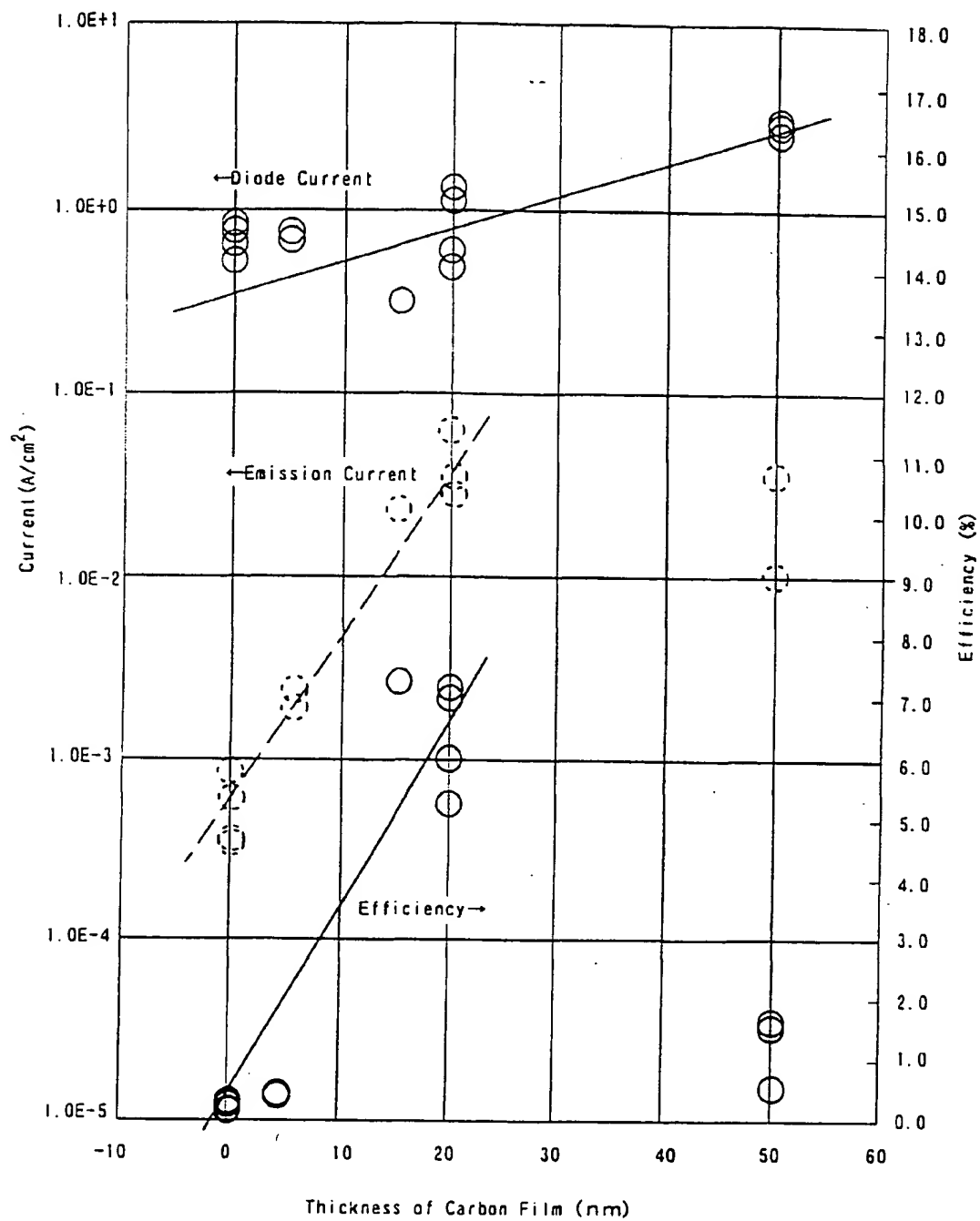


Fig. 28

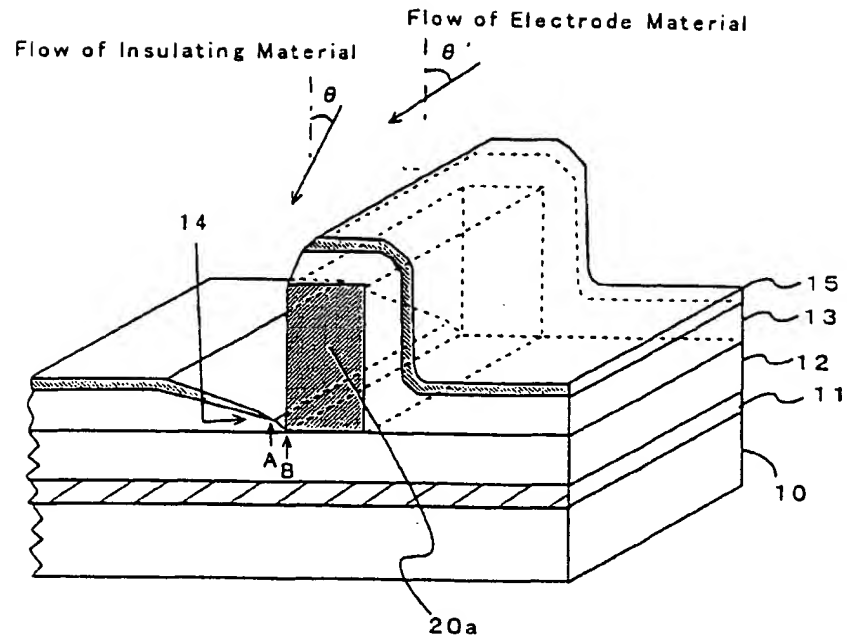


Fig. 29

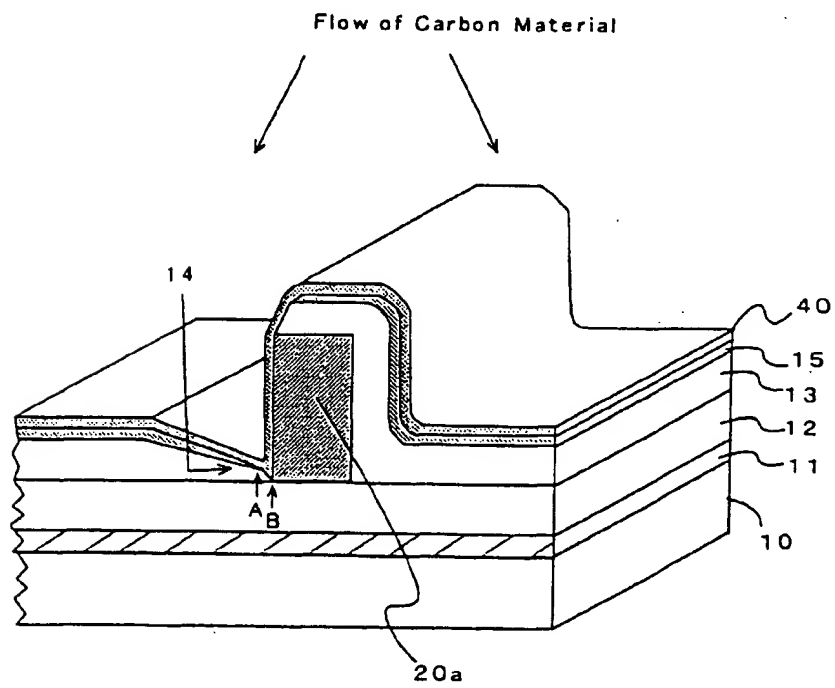


Fig.30

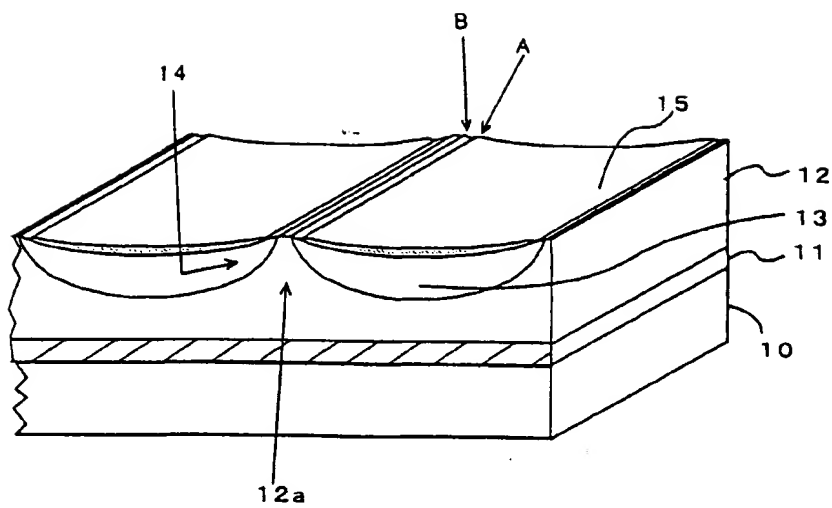


Fig.31

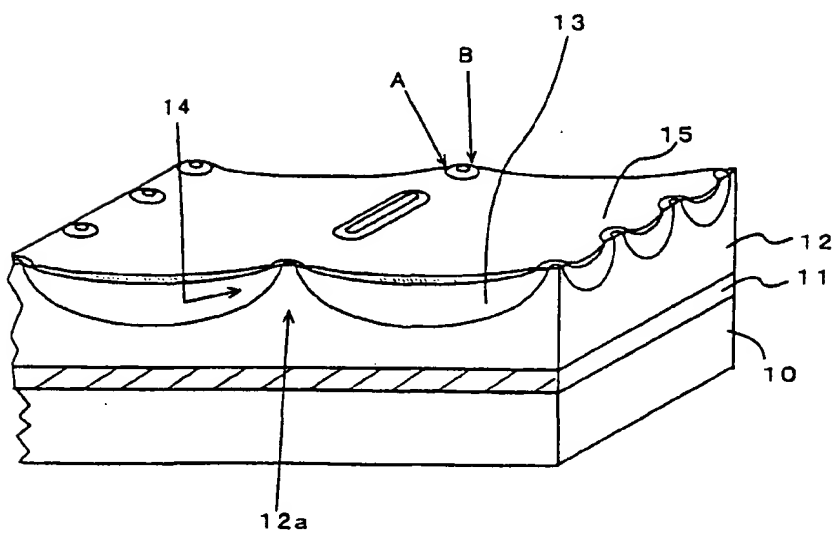
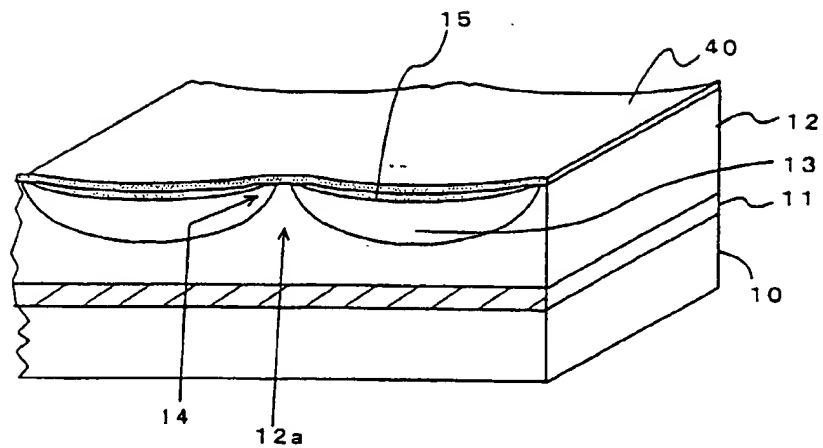


Fig.32



**Fig.33**

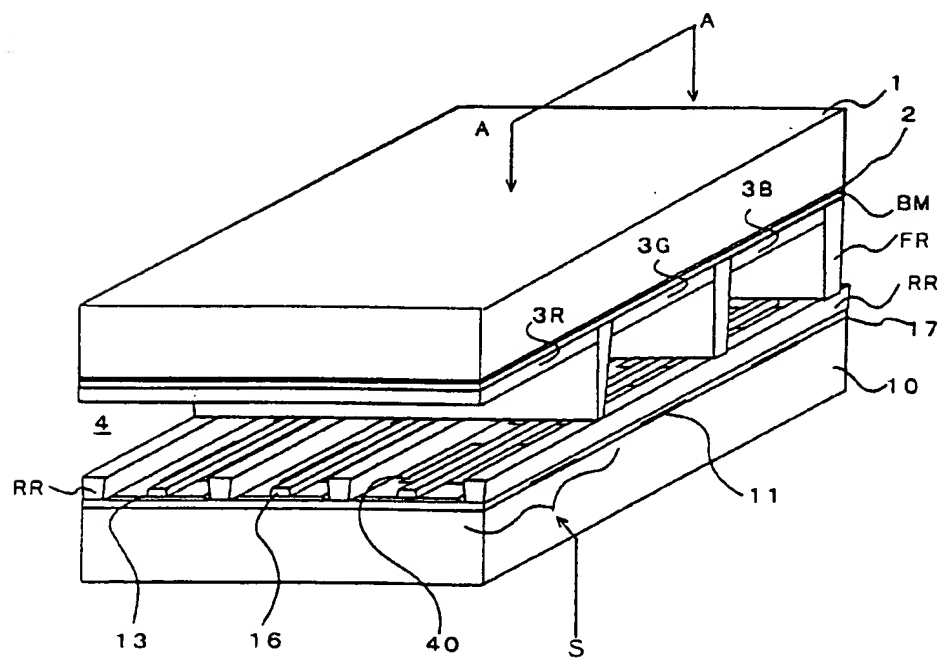


Fig.34

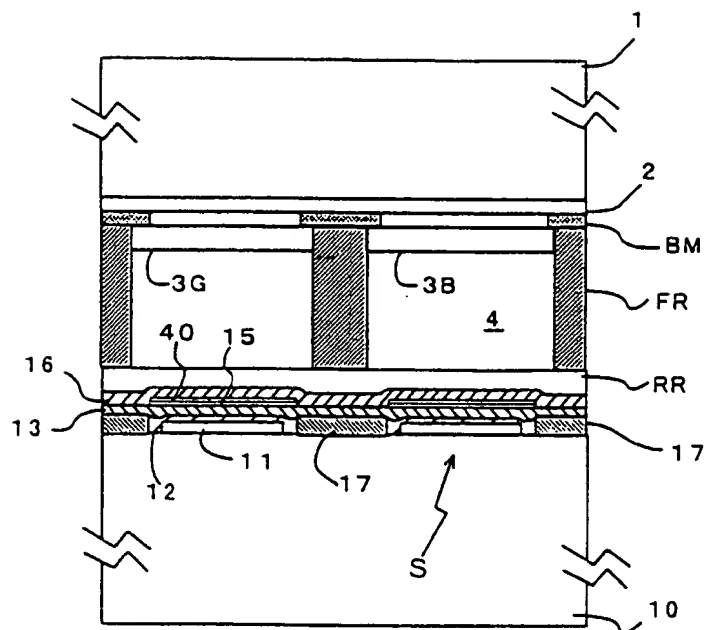


Fig.35

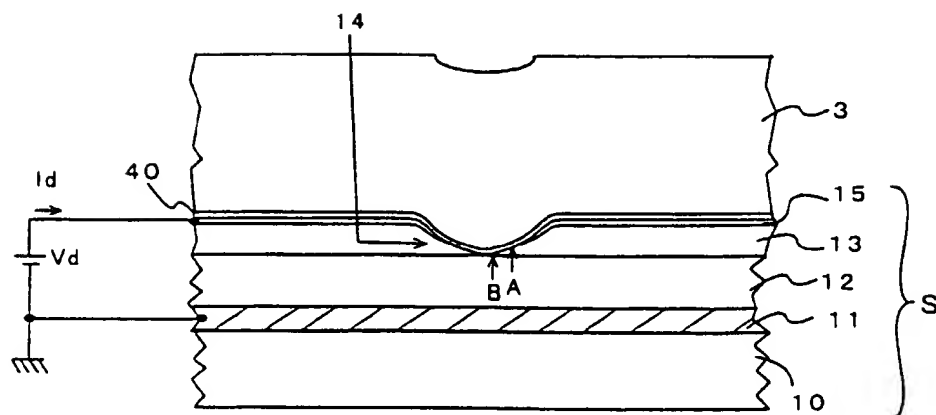
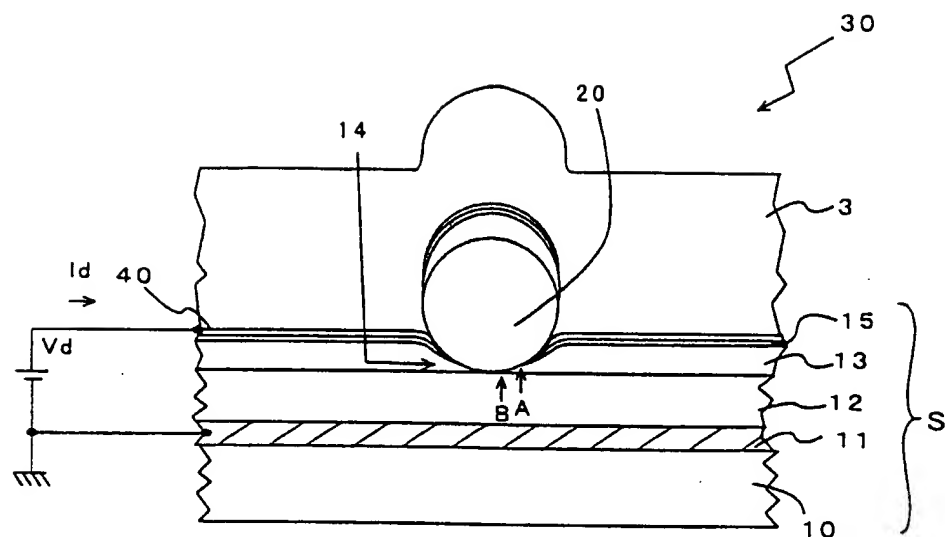




Fig.36





European Patent  
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## EUROPEAN SEARCH REPORT

Application Number  
EP 01 10 0013

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CI.7)
P, A	EP 0 986 084 A (PIONEER CORP) 15 March 2000 (2000-03-15) * claims 1, 10, 19 *	1, 17, 27, 30	H01J1/312 H01J9/02
A	EP 0 367 195 A (MATSUSHITA ELECTRIC IND CO LTD) 9 May 1990 (1990-05-09) * claims 1, 23 *	1, 17	
A	FR 2 084 551 A (PHILIPS NV) 17 December 1971 (1971-12-17) * claims 1-6 *	1, 17	
A	US 3 535 598 A (FEIST WOLFGANG M) 20 October 1970 (1970-10-20)		
A	US 3 184 659 A (J. COHEN) 18 May 1965 (1965-05-18)		
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.CI.7)
			H01J
Place of search:		Date of completion of the search	Examiner
THE HAGUE		23 April 2001	Van den Bulcke, E
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EPO FORM 1503 03 82 (12/04/2001)

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 10 0013

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

23-04-2001 "

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0986084 A	15-03-2000	CN 1264151 A JP 2000149766 A	23-08-2000 30-05-2000
EP 0367195 A	09-05-1990	JP 2121227 A JP 2126531 A JP 2126532 A JP 2170327 A JP 2172127 A US 5202605 A	09-05-1990 15-05-1990 15-05-1990 02-07-1990 03-07-1990 13-04-1993
FR 2084551 A	17-12-1971	DE 2012192 A CA 937977 A GB 1340353 A JP 52024829 B NL 7103154 A	07-10-1971 04-12-1973 12-12-1973 04-07-1977 16-09-1971
US 3535598 A	20-10-1970	NONE	
US 3184659 A	18-05-1965	NONE	

EPO FORM F0453

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82